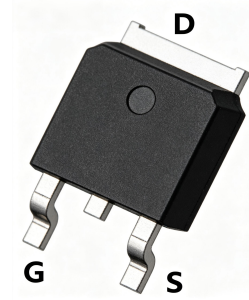
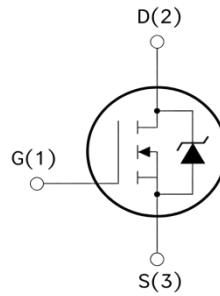


Silicon Carbide Power MOSFET

Parameter	Value	Unit
V_{DS}	650	V
I_D	9.3	A
$R_{DS(ON)}$	540	m Ω
Q_G	27	nC



TO-252

Features

- High Speed Switching with Low Capacitances
- High Blocking Voltage with Low $R_{DS(on)}$
- Low impedance package with driver source pin
- Easy to parallel and simple to drive

Applications

- Motor Drives
- Battery Chargers
- Photovoltaic-storage-charging
- High Voltage DC/DC Converters
- Switched-Mode Power Supply(SMPS)

Absolute Maximum Ratings (at $T_J=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Value	Unit
Drain-source Voltage	V_{DS}	$V_{GS}=0V$	650	V
Gate-source Voltage	V_{GS}	Absolute maximum values	-10/+22	V
	V_{GSop}	Recommended operational values	0/+18	
Drain Current (continuous)	I_D	$V_{GS}=15V; T_C=25^\circ\text{C}$	9.3	A
		$V_{GS}=15V; T_C=175^\circ\text{C}$	6.6	
Drain Current (pulsed)	I_{DM}	$V_{GS}=15V; T_C=25^\circ\text{C}$	19	A
Power Dissipation	P_D	$T_C=25^\circ\text{C}; T_J=175^\circ\text{C}$	52	W
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-	-55 to +175	$^\circ\text{C}$
Thermal Resistance from Junction to Case	$R_{\theta JC}$	-	2.9	$^\circ\text{C/W}$

Electrical Characteristics

Parameter	Symbol	Conditions	Value			Unit
			Min.	Typ.	Max.	
Static characteristics (at $T_C=25^\circ\text{C}$ unless otherwise specified)						
Drain-Source Breakdown Voltage	$B_{V_{DS}}$	$V_{GS}=0V; I_D=500\mu\text{A}$	650	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{GS}=V_{DS}; I_{DS}=1.3\text{mA}$	2.6	-	4.6	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=650V; V_{GS}=0V; T_J=25^\circ\text{C}$	-	-	10	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS}=18V; V_{DS}=0V$	-	-	250	nA
Static Drain-Source on Resistance	$R_{DS(on)}$	$V_{GS}=15V; I_D=3.6\text{A}$	-	540	700	m Ω
		$V_{GS}=15V; I_D=3.6\text{A}; T_J=175^\circ\text{C}$	-	460	-	
		$V_{GS}=18V; I_D=3.6\text{A}$	-	380	-	
		$V_{GS}=18V; I_D=3.6\text{A}; T_J=175^\circ\text{C}$	-	387	-	
Dynamic characteristics (at $T_C=25^\circ\text{C}$ unless otherwise specified)						
Input Capacitance	C_{iss}	$V_{DS}=500V; f=100\text{KHz}; V_{GS}=0V; T_J=25^\circ\text{C}$	-	190	-	pF
Output Capacitance	C_{oss}		-	15	-	
Reverse Transfer Capacitance	C_{rss}		-	1.65	-	
Total Gate Charge	Q_G	$V_{DS}=500V; V_{GS}=0/+15V; I_D=3.6\text{A}; T_J=25^\circ\text{C}$	-	27	-	nC
Gate-Source Charge	Q_{GS}		-	9.5	-	
Gate-Drain Charge	Q_{GD}		-	3.0	-	
Gate Resistor	R_G	$f=1\text{MHz}$	-	7.5	-	Ω
Turn-on Delay Time	$t_{d(on)}$	$V_{DS}=500V; V_{GS}=0/+18V; I_D=3.6\text{A}; R_{g(ext)}=10\Omega$	-	15	-	ns
Rise Time	t_r		-	39	-	
Turn-off Delay Time	$t_{d(off)}$		-	16	-	
Fall Time	t_f		-	69	-	

Reverse SiC Diode Characteristics(at $T_J=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Values			Units
			Min.	Typ.	Max.	
Diode Forward Voltage	V_{FSD}	$V_{GS}=0V; I_F=3\text{A}; T_J=25^\circ\text{C}$	-	3.5	-	V
Continuous Diode Forward Current	I_S	$V_{GS}=0V; T_J=25^\circ\text{C}$	-	9.3	-	A
Reverse Recovery Time	t_{RR}	$V_R=500V; V_{GS}=0V; I_F=3.6\text{A}; di/dt=850\text{A}/\mu\text{s}; T_J=25^\circ\text{C}$	-	11	-	ns
Reverse Recovery Charge	Q_{RR}		-	35	-	nC
Peak Reverse Recovery Current	I_{RRM}		-	5.5	-	A

Typical Characteristics

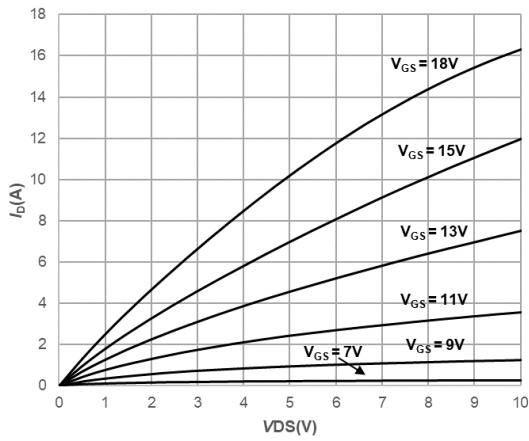


Fig1. Output Characteristics $T_j=25^\circ\text{C}$

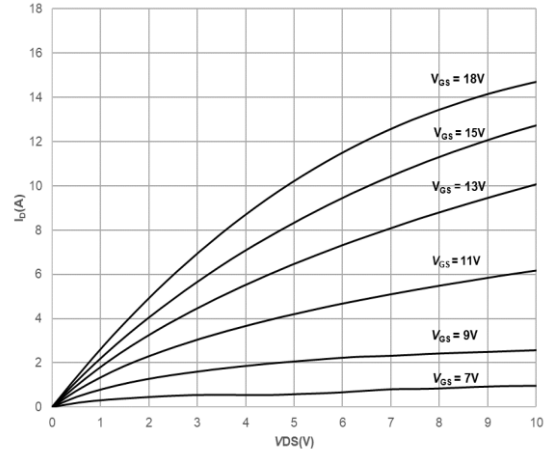


Fig2. Output Characteristics $T_j=175^\circ\text{C}$

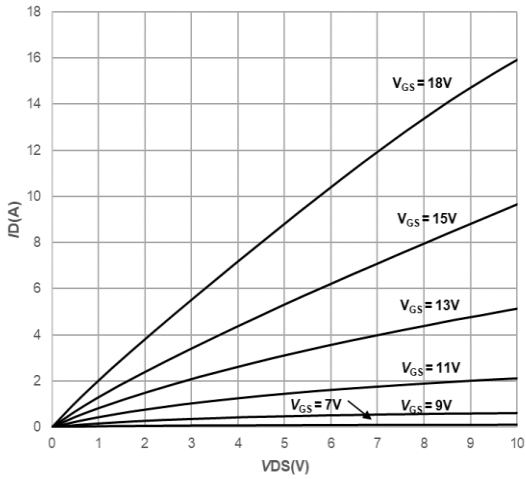


Fig2. Output Characteristics $T_j=-40^\circ\text{C}$

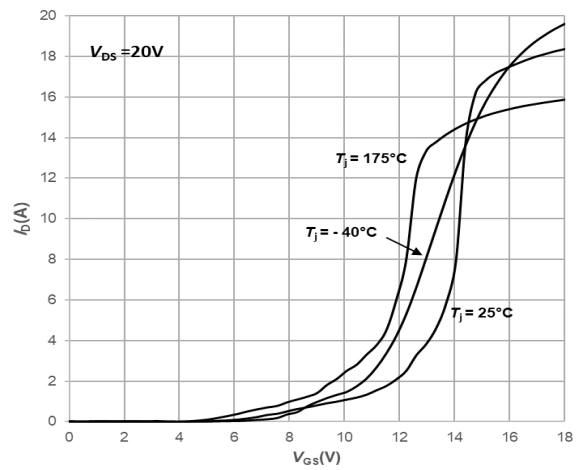


Fig3. Typical Transfer Characteristics

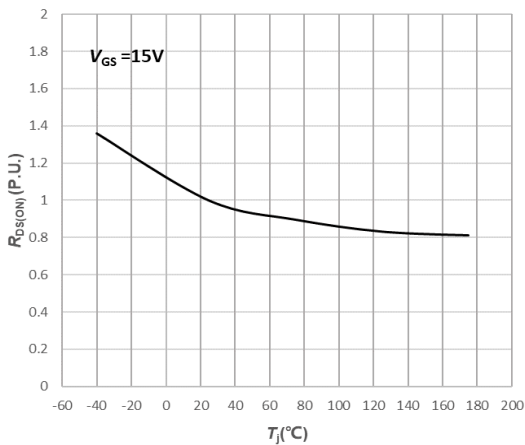


Fig4. Normalized On-Resistance vs. Temperature

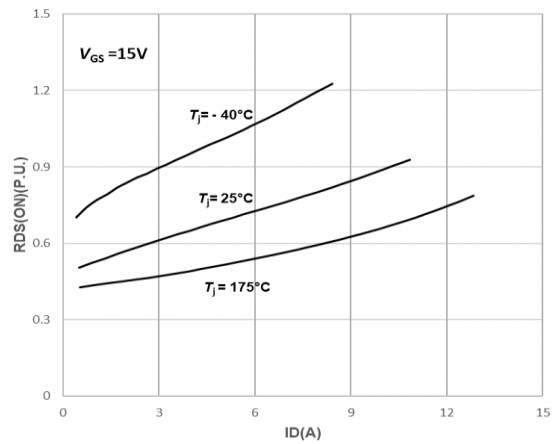


Fig5. Normalized On-Resistance vs. Drain Current For Various Temperatures

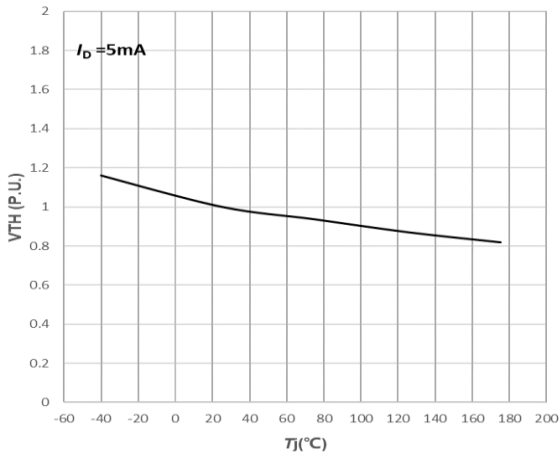


Fig6. Normalized Threshold Voltage vs. Temperature

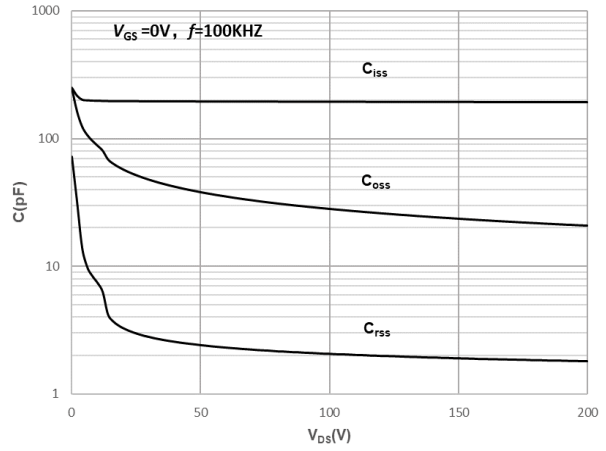


Fig7. Capacitances vs. Drain-Source Voltage (0-200V)

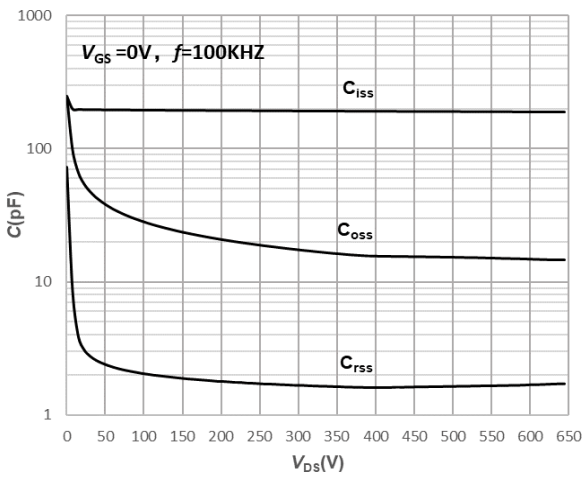


Fig8. Capacitances vs. Drain-Source Voltage (0-650V)

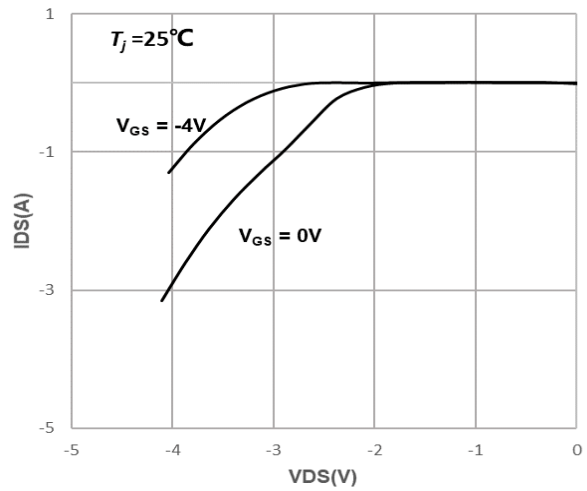
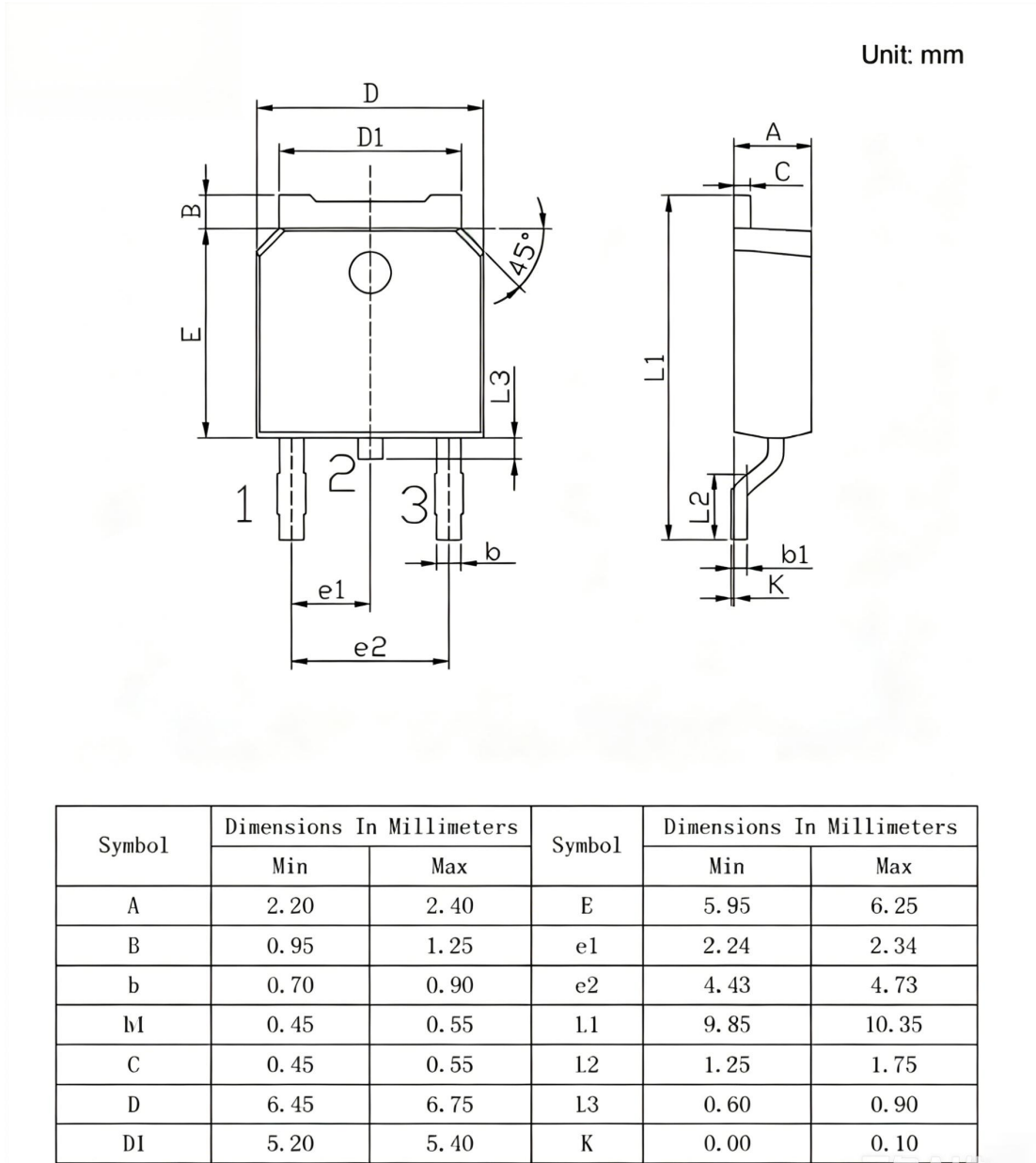


Fig9. Body Diode Characteristics

Package Outlines(Unit:mm)

TO-252



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