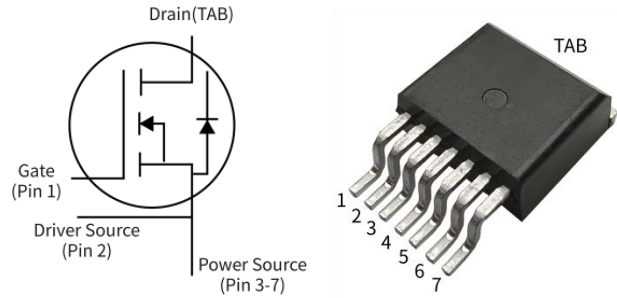


Silicon Carbide Power MOSFET

Parameter	Value	Unit
V_{DS}	1700	V
I_D	4.9	A
$R_{DS(ON)}$	1.0	Ω
Q_G	15.6	nC



TO-263-7L

Features

- High Speed Switching with Low Capacitances
- High Blocking Voltage with Low $R_{DS(on)}$
- Low impedance package with driver source pin
- Easy to parallel and simple to drive

Applications

- EV Charging
- High Voltage DC/DC Converters
- Switched-Mode Power Supply(SMPS)
- Power Factor Correction(PFC)

Absolute Maximum Ratings (at $T_J=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-source Voltage	V_{DS}	1700	V
Gate-source Voltage	V_{GSmax}	-10/+25	V
Gate-Source Voltage	V_{GSop}	-5/+20	V
Drain Current (continuous; $T_c=25^\circ\text{C}$)	I_D	4.9	A
Drain Current (continuous; $T_c=100^\circ\text{C}$)		3.0	
Drain Current (pulsed)	I_{DM}	16	A
Power Dissipation ($T_c=25^\circ\text{C}$)	P_D	78	W
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 to +150	$^\circ\text{C}$
Thermal Resistance from Junction to Case	$R_{\theta JC}$	1.6	$^\circ\text{C/W}$
Thermal Resistance From Junction to Ambient	$R_{\theta JA}$	58	

Electrical Characteristics

Parameter	Symbol	Conditions	Value			Unit
			Min.	Typ.	Max.	
Static characteristics (at $T_C=25^\circ\text{C}$ unless otherwise specified)						
Drain-Source Breakdown Voltage	$B_{V_{DS}}$	$V_{GS}=0V; I_D=100\mu A$	1700	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=1700V; V_{GS}=0V$	-	-	100	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS}=20V; V_{DS}=0V$	-	-	250	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{GS}=V_{DS}; I_D=500\mu A$	2.0	2.7	4.5	V
Static Drain-Source on Resistance	$R_{DS(on)}$	$V_{GS}=20V; I_D=2A$	-	1.0	1.5	Ω
		$V_{GS}=20V; I_D=2A; T_J=150^\circ\text{C}$	-	1.8	3.0	
Dynamic characteristics (at $T_C=25^\circ\text{C}$ unless otherwise specified)						
Input Capacitance	C_{iss}	$V_{DS}=1000V; f=1\text{MHz}; T_J=25^\circ\text{C}$	-	155	-	pF
Output Capacitance	C_{oss}		-	21	-	
Reverse Transfer Capacitance	C_{rss}		-	4.6	-	
Transconductance	g_{fs}	$V_{DS}=20V; I_D=2A$	-	0.9	-	S
		$V_{DS}=20V; I_D=2A; T_J=150^\circ\text{C}$	-	0.87	-	
C_{oss} Stored Energy	E_{oss}	$V_{DS}=1000V; f=1\text{MHz}; T_J=25^\circ\text{C}$	-	8	-	μJ
Total Gate Charge	Q_G	$V_{DS}=1000V; V_{GS}=-5/20V; I_D=2A$	-	15.6	-	nC
Gate-Source Charge	Q_{GS}		-	1.8	-	
Gate-Drain Charge	Q_{GD}		-	9.7	-	
Internal Gate Resistor	R_{Gint}	$f=1\text{MHz}; V_{AC}=25\text{mV}$	-	12	-	Ω
Turn-on Delay Time	$t_{d(on)}$	$V_{DS}=1000V; V_{GS}=-5/20V; I_D=2A; R_G=0\Omega;$	-	14	-	ns
Rise Time	t_r		-	16	-	
Turn-off Delay Time	$t_{d(off)}$		-	15	-	
Fall Time	t_f		-	66	-	

Reverse SiC Diode Characteristics(at $T_J=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Values			Units
			Min.	Typ.	Max.	
Reverse Recovery Time	t_{RR}	$V_R=1000V; I_F=2A; di/dt=1000A/\mu s$	-	6.8	-	ns
Reverse Recovery Charge	Q_{RR}		-	13.7	-	nC
Peak Reverse Recovery Current	I_{RRM}		-	3.5	-	A

Typical Characteristics

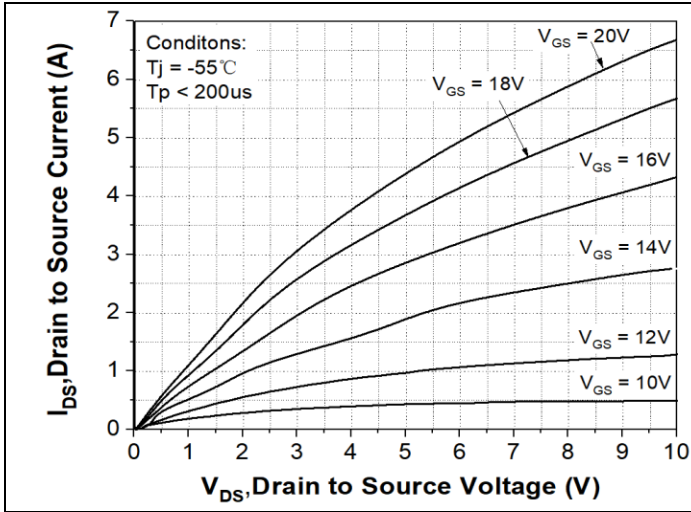


Figure 1. Output Characteristics $T_J = -55^\circ\text{C}$

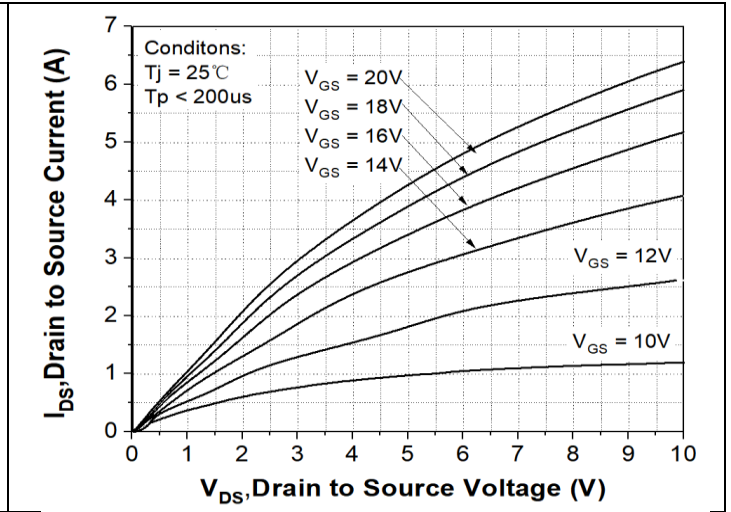


Figure 2. Output Characteristics $T_J = 25^\circ\text{C}$

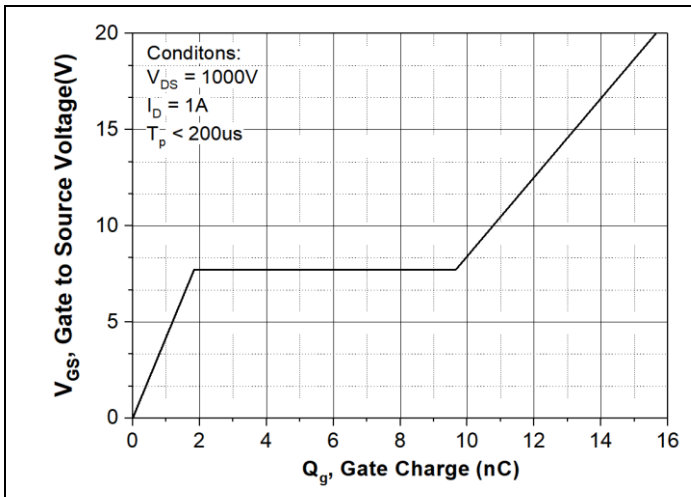


Figure 3. Gate Charge Characteristic

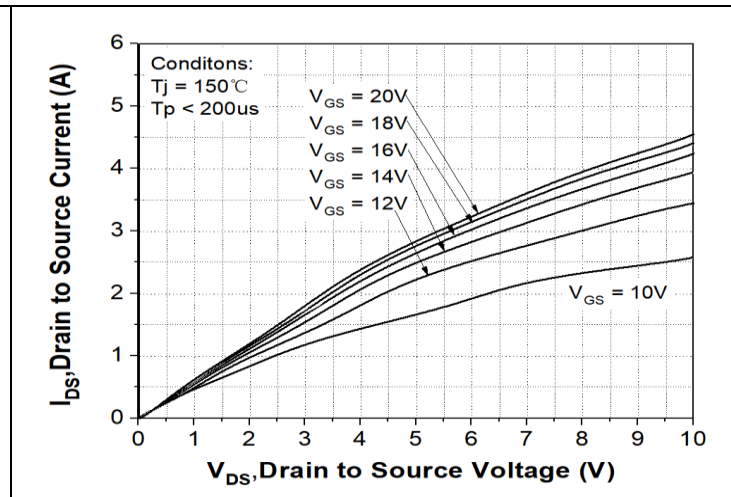


Figure 4. Output Characteristics $T_J = 150^\circ\text{C}$

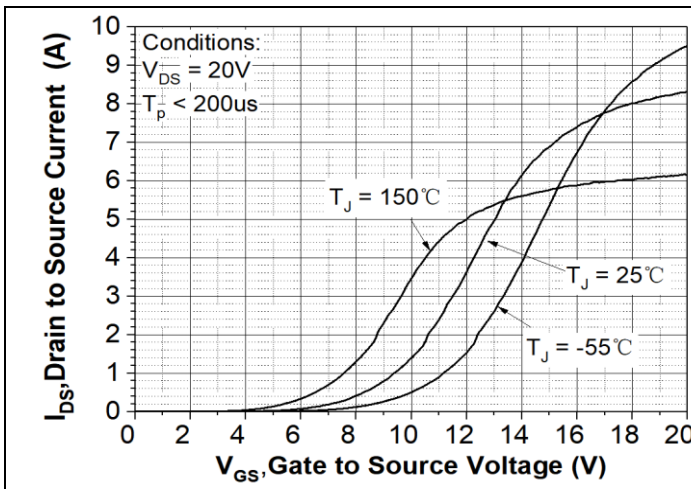


Figure 5. Transfer Characteristic for Various Junction Temperatures

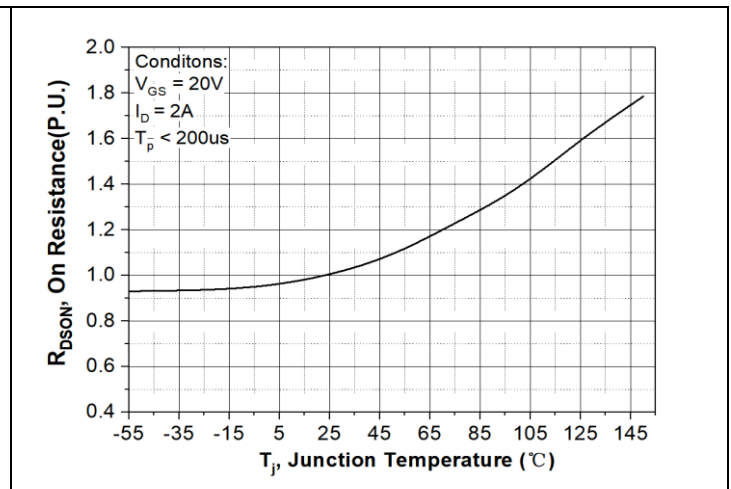


Figure 6. Normalized On-Resistance vs. Temperature

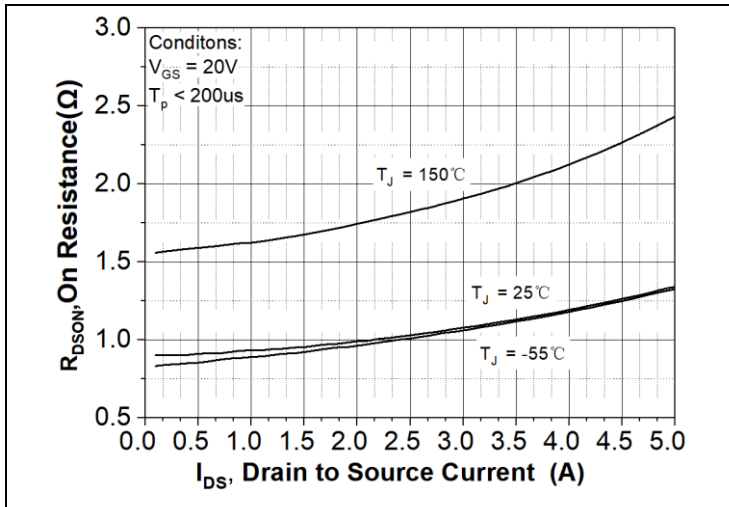


Figure 7. On-Resistance vs. Drain Current For Various Temperatures

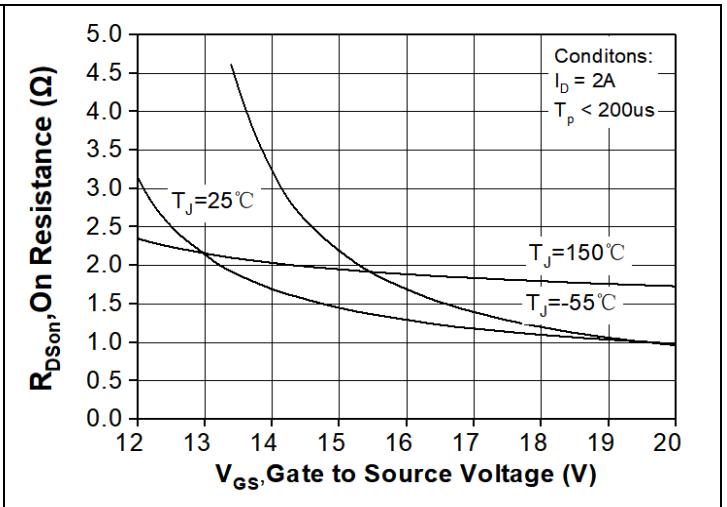


Figure 8. On-Resistance vs. Gate Voltage For Various Temperatures

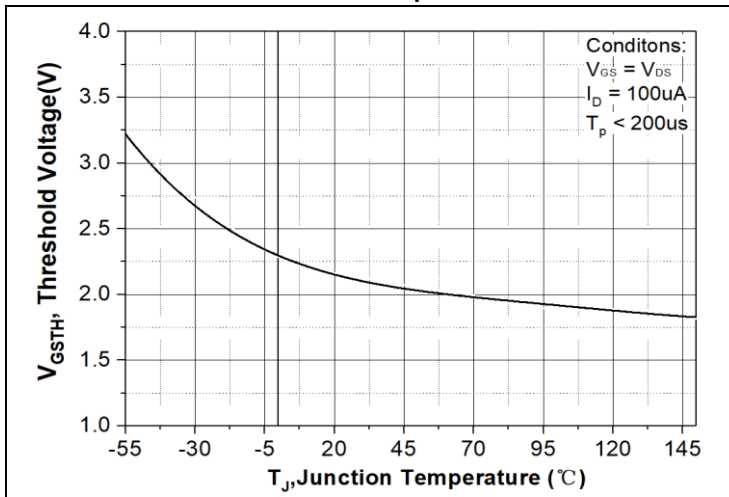


Figure 9. Threshold Voltage vs. Temperature

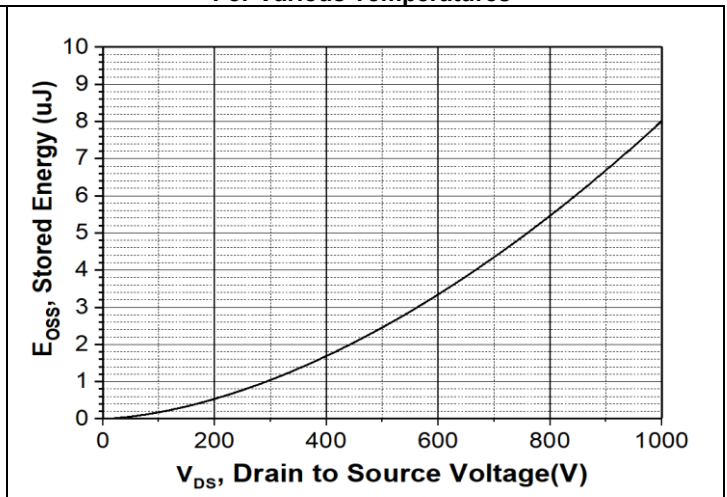


Figure 10. Output Capacitor Stored Energy

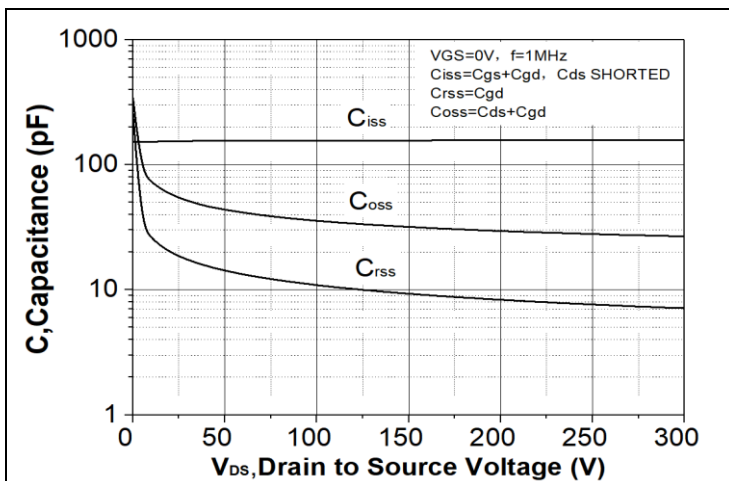


Figure 11. Capacitances vs. Drain-Source Voltage (0 - 300V)

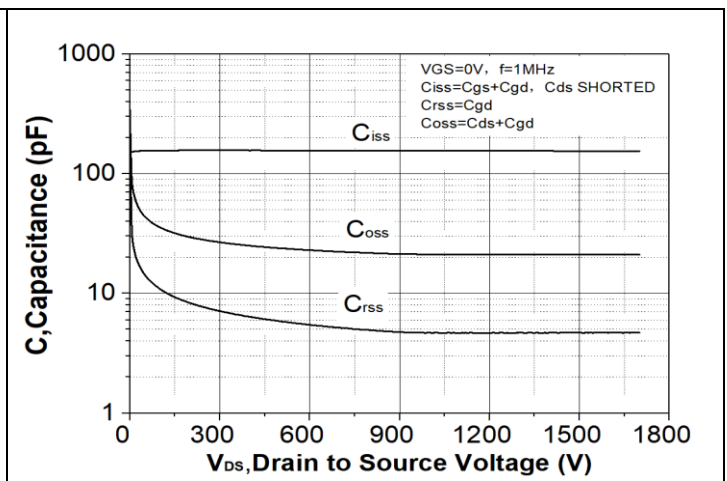


Figure 12. Capacitances vs. Drain-Source Voltage (0 - 1200V)

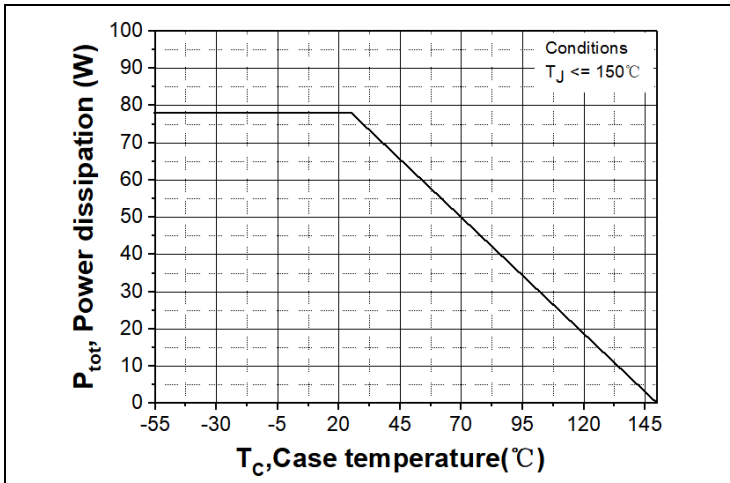


Figure 13. Maximum Power Dissipation Derating vs. Case Temperature

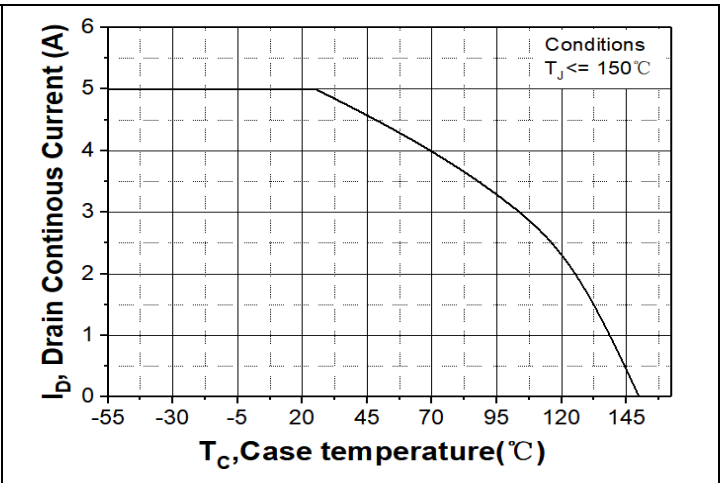


Figure 14. Continuous Drain Current Derating vs. Case Temperature

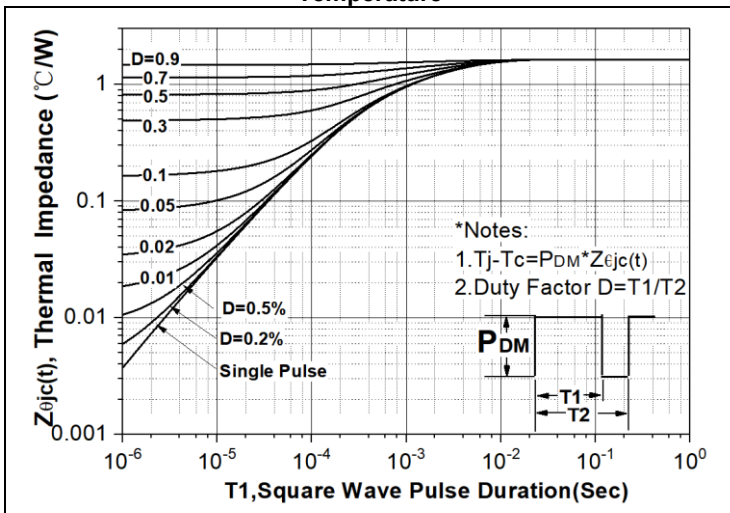


Figure 15. Transient Thermal Impedance (Junction - Case)

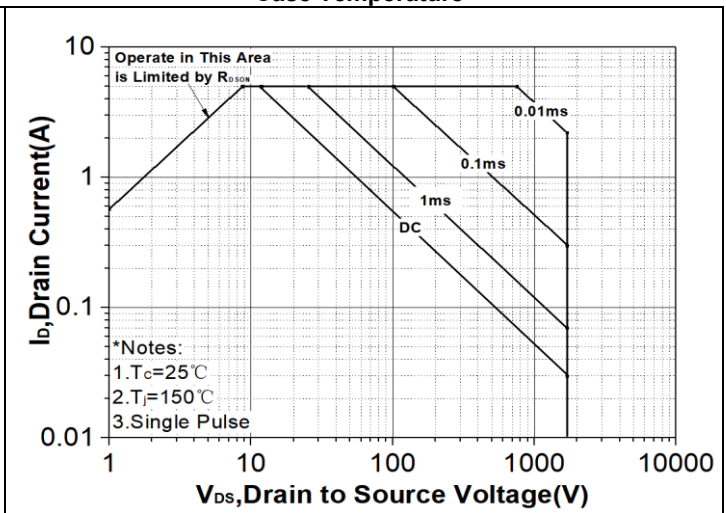


Figure 16. Safe Operating Area

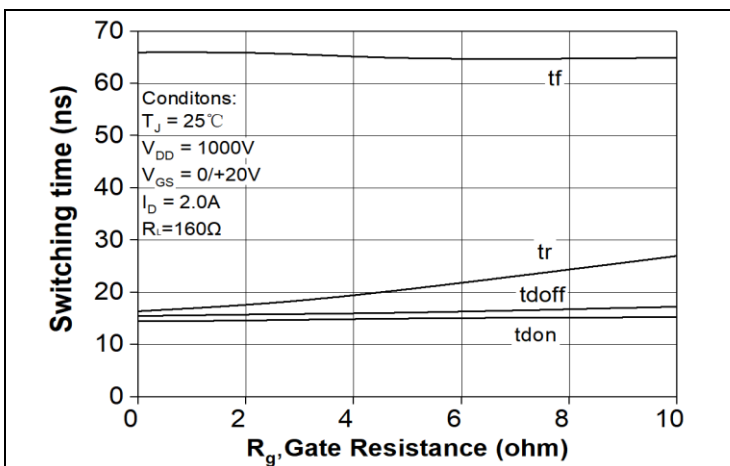
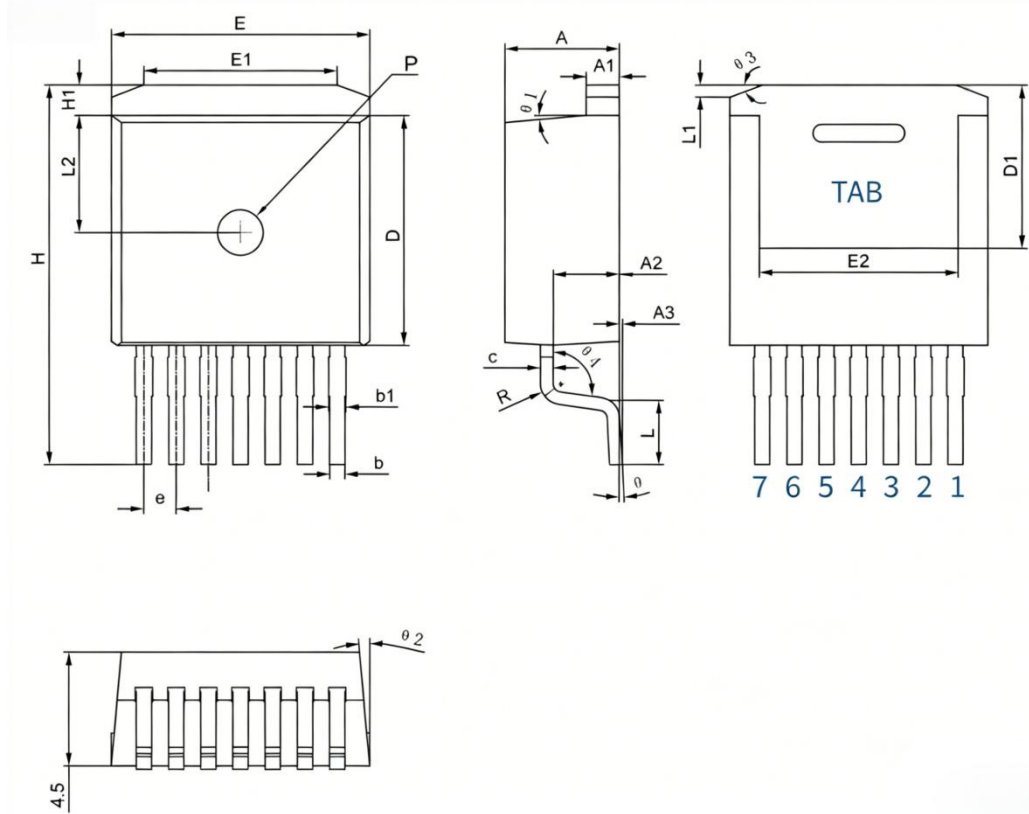


Figure 17. Resistive Switching Times vs. R_G(ext)

Package Outlines(Unit:mm)

TO-263-7L



Symbol	Millimeters			Symbol	Millimeters		
	Min.	Type.	Max.		Min.	Type.	Max.
A	4.40	4.50	4.60	e	1.17	1.27	1.37
A1	1.25	1.30	1.40	H	14.75	15.00	15.25
A2	2.45	2.60	2.70	H1	1.10	1.20	1.30
A3	0.05	0.13	0.20	L	2.35	2.55	2.75
b	0.50	0.60	0.70	L1	0.37	0.57	0.77
b1	0.60	0.70	0.85	L2	4.48	4.63	4.78
c	0.45	0.50	0.60	θ	0°	3°	5°
D	8.88	9.08	9.28	θ1	3°	5°	7°
D1	6.25	6.45	6.65	θ2	3°	5°	7°
E	9.88	10.18	10.28	θ3	15°	20°	25°
E1	6.67	7.07	7.47	R	0.75	0.80	0.85
E2	7.67	7.82	7.97	P	1.70	1.80	1.90

Pin	Symbol	Description
1	G	Gate
2	KS	Driver Source
3-7	S	Power Source
TAB	D	Drain

Note:

1. All metal surfaces are Sn plated (matte), except area of cut.
2. Burr or mold flash size (0.5 mm) is not included in the dimensions.

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