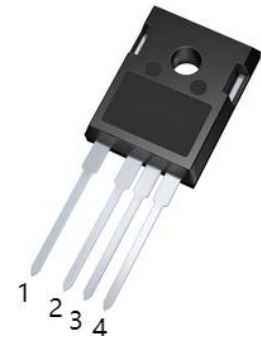
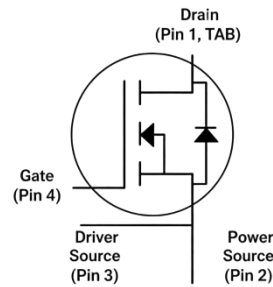


## Silicon Carbide Power MOSFET

Parameter	Value	Unit
$V_{DS}$	1700	V
$I_D$	4.8	A
$R_{DS(ON)}$	1.0	$\Omega$
$Q_G$	15.6	nC



TO-247-4L

### Features

- High Blocking Voltage with Low On-Resistance
- High Speed Switching with Low Capacitances
- Avalanche Ruggedness
- Easy to Parallel and Simple to Drive

### Applications

- EV Charging
- High Voltage DC/DC Converters
- Switched-Mode Power Supply(SMPS)
- Power Factor Correction(PFC)

### Absolute Maximum Ratings (at $T_J=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-source Voltage	$V_{DS}$	1700	V
Gate-source Voltage (Absolute maximum values)	$V_{GS}$	-10/+25	V
Gate-source Voltage (Recommended operational values)		-5/+20	
Drain Current (continuous; $T_c=25^\circ\text{C}$ ) $V_{GS}=20\text{V}$	$I_D$	4.8	A
Drain Current (continuous; $T_c=100^\circ\text{C}$ ) $V_{GS}=20\text{V}$		2.9	
Drain Current (pulsed)	$I_{DM}$	16	A
Power Dissipation ( $T_c=25^\circ\text{C}$ , $T_J=150^\circ\text{C}$ )	$P_D$	74	W
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 to +150	$^\circ\text{C}$
Thermal Resistance from Junction to Case	$R_{\theta JC}$	1.68	$^\circ\text{C/W}$
Thermal Resistance From Junction to Ambient	$R_{\theta JA}$	55	

**Electrical Characteristics**

Parameter	Symbol	Conditions	Value			Unit
			Min.	Typ.	Max.	
<b>Static characteristics</b> (at $T_C=25^\circ\text{C}$ unless otherwise specified)						
Drain-Source Breakdown Voltage	$B_{V_{DS}}$	$V_{GS}=0V; I_D=100\mu A$	1700	-	-	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=1700V; V_{GS}=0V$	-	-	100	$\mu A$
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=1700V; V_{GS}=0V; T_J=150^\circ\text{C}$	-	-	200	$\mu A$
Gate-Body Leakage Current	$I_{GSS}$	$V_{GS}=20V; V_{DS}=0V$	-	-	250	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{GS}=V_{DS}; I_D=500\mu A; T_J=25^\circ\text{C}$	2.0	2.7	4.5	V
Static Drain-Source on Resistance	$R_{DS(on)}$	$V_{GS}=20V; I_D=2A; T_J=25^\circ\text{C}$	-	1.0	1.5	$\Omega$
		$V_{GS}=20V; I_D=2A; T_J=150^\circ\text{C}$	-	1.8	3.0	
Transconductance	$G_{fs}$	$V_{GS}=20V; I_D=2A; T_J=25^\circ\text{C}$	-	0.9	-	S
		$V_{GS}=20V; I_D=2A; T_J=150^\circ\text{C}$	-	0.87	-	
<b>Dynamic characteristics</b> (at $T_C=25^\circ\text{C}$ unless otherwise specified)						
Input Capacitance	$C_{iss}$	$V_{DS}=1000V; f=1\text{MHz}; V_{GS}=0V$ $T_J=25^\circ\text{C}$	-	155	-	$\mu F$
Output Capacitance	$C_{oss}$		-	21	-	
Reverse Transfer Capacitance	$C_{rss}$		-	4.6	-	
$C_{oss}$ Stored Energy	$E_{oss}$		-	8	-	
Total Gate Charge	$Q_G$	$V_{DD}=1000V; V_{GS}=-5/20V;$ $I_D=2A; T_J=25^\circ\text{C}$	-	15.6	-	nC
Gate-Source Charge	$Q_{GS}$		-	1.8	-	
Gate-Drain Charge	$Q_{GD}$		-	9.7	-	
Internal Gate Resistor	$R_{Gint}$	$f=1\text{MHz}; V_{AC}=25\text{mV}$	-	12	-	$\Omega$
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=100V; V_{GS}=-5/20V;$ $I_D=25A; R_{g(ext)}=0\Omega$	-	14	-	ns
Rise Time	$t_r$		-	16	-	
Turn-off Delay Time	$t_{d(off)}$		-	15	-	
Fall Time	$t_f$		-	66	-	

**Reverse SiC Diode Characteristics**(at  $T_J=25^\circ\text{C}$  unless otherwise specified)

Parameter	Symbol	Conditions	Values			Units
			Min.	Typ.	Max.	
Reverse Recovery Time	$t_{RR}$	$V_R=1000V; V_{GS}=0V; I_F=2A;$ $di/dt=1000A/\mu s, T_J=25^\circ\text{C}$	-	6.8	-	ns
Reverse Recovery Charge	$Q_{RR}$		-	13.7	-	nC
Peak Reverse Recovery Current	$I_{RRM}$		-	3.5	-	A

## Typical Characteristics

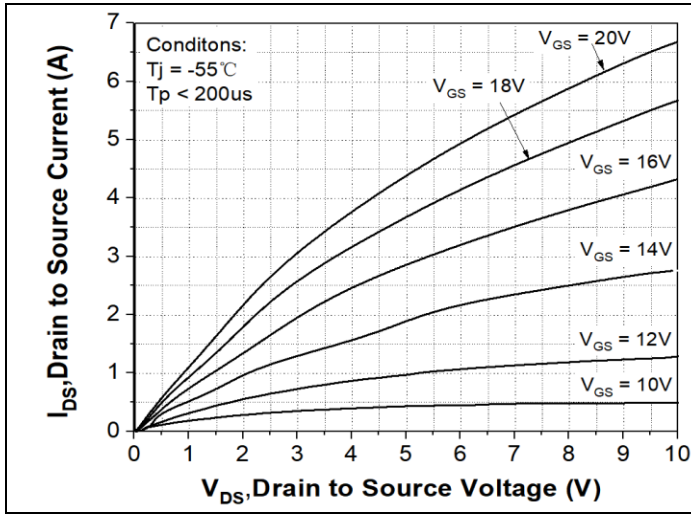


Figure 1. Output Characteristics  $T_J = -55^\circ\text{C}$

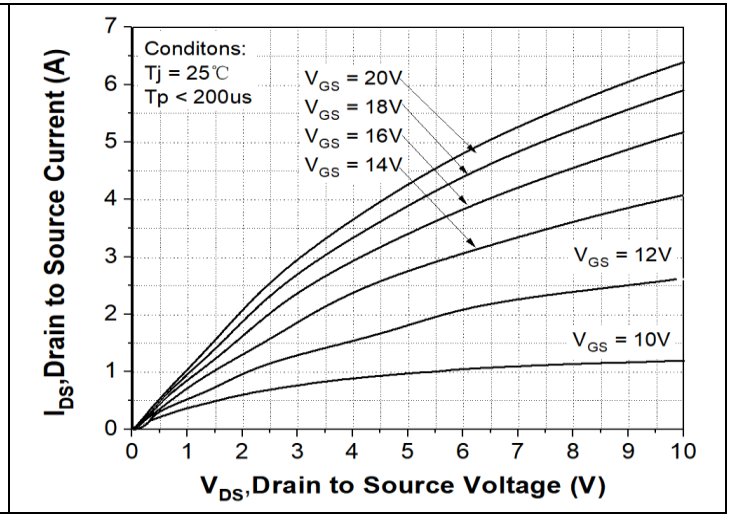


Figure 2. Output Characteristics  $T_J = 25^\circ\text{C}$

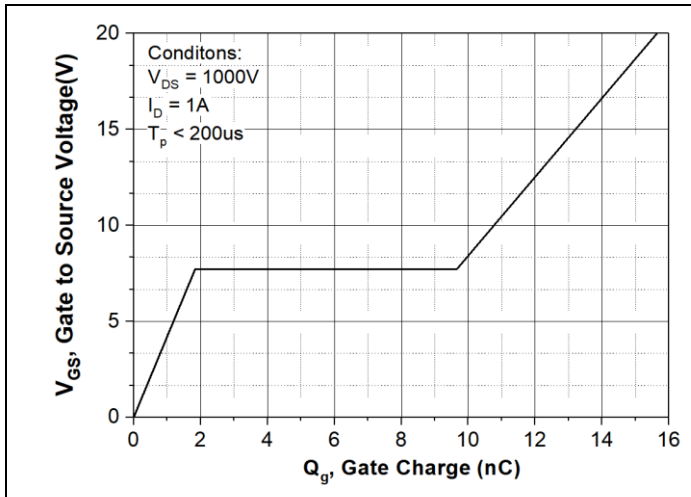


Figure 3. Gate Charge Characteristic

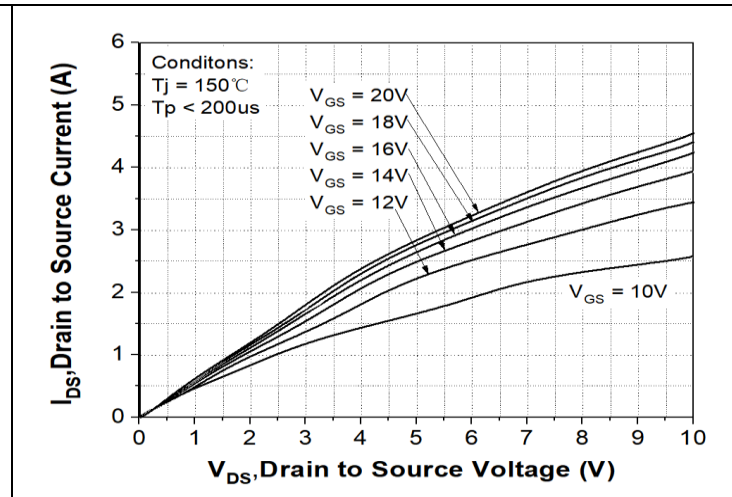


Figure 4. Output Characteristics  $T_J = 150^\circ\text{C}$

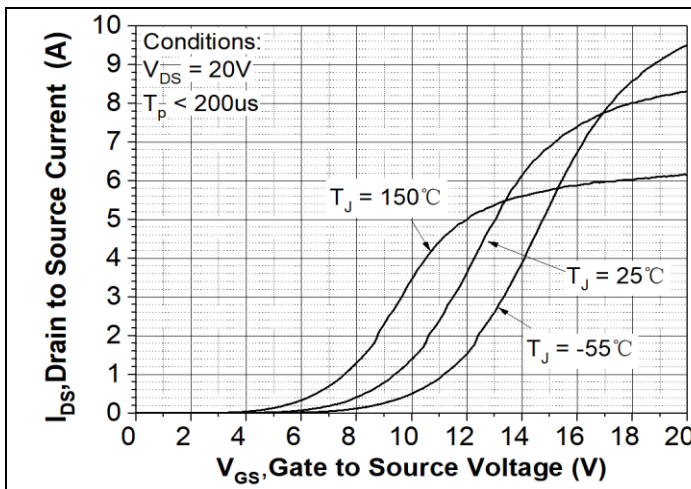


Figure 5. Transfer Characteristic for Various Junction Temperatures

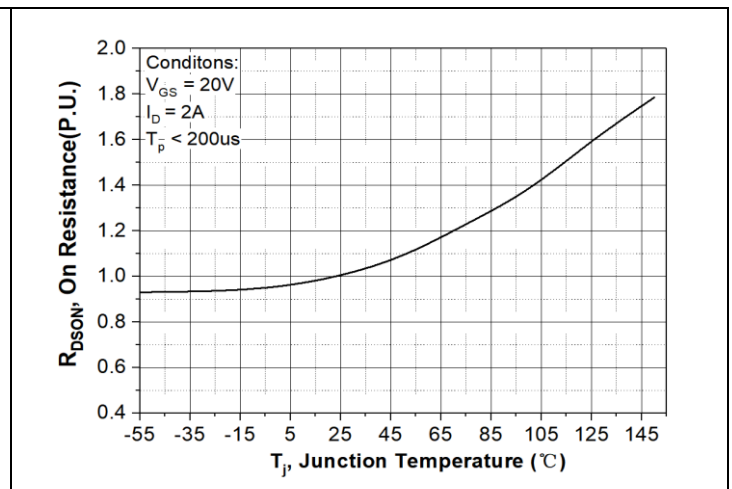


Figure 6. Normalized On-Resistance vs. Temperature

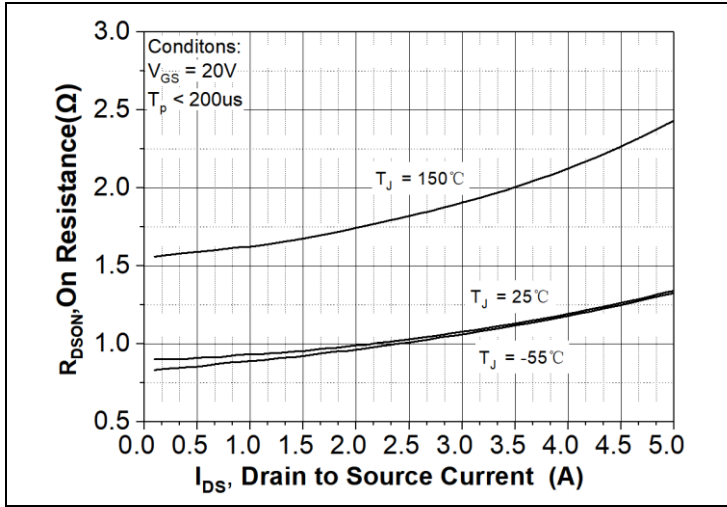


Figure 7. On-Resistance vs. Drain Current For Various Temperatures

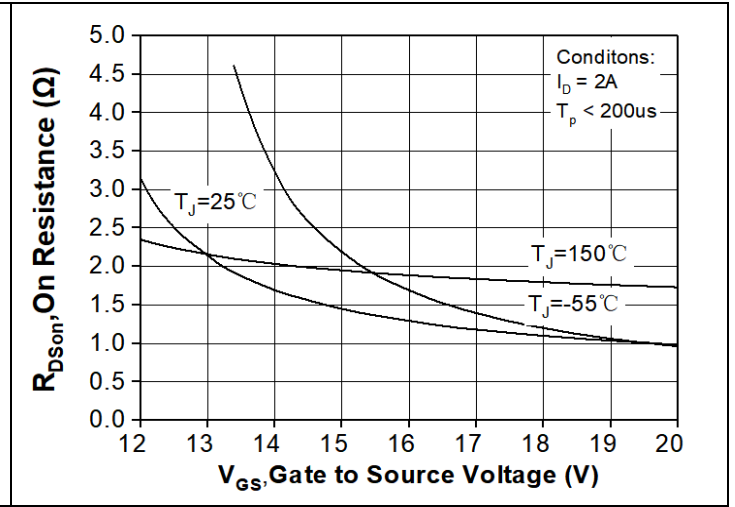


Figure 8. On-Resistance vs. Gate Voltage For Various Temperatures

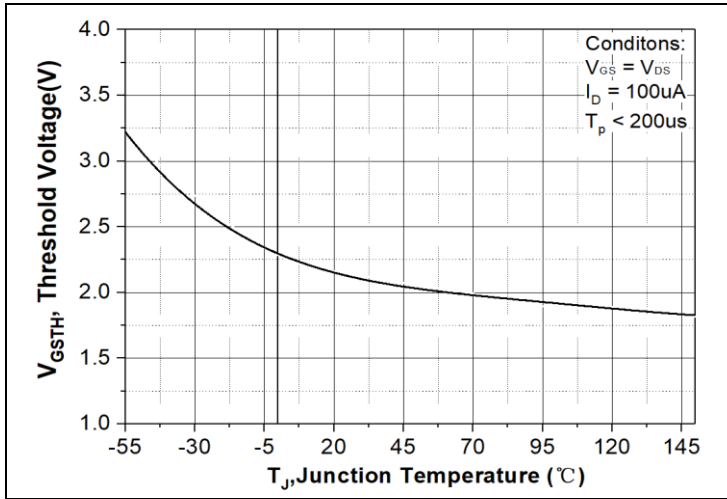


Figure 9. Threshold Voltage vs. Temperature

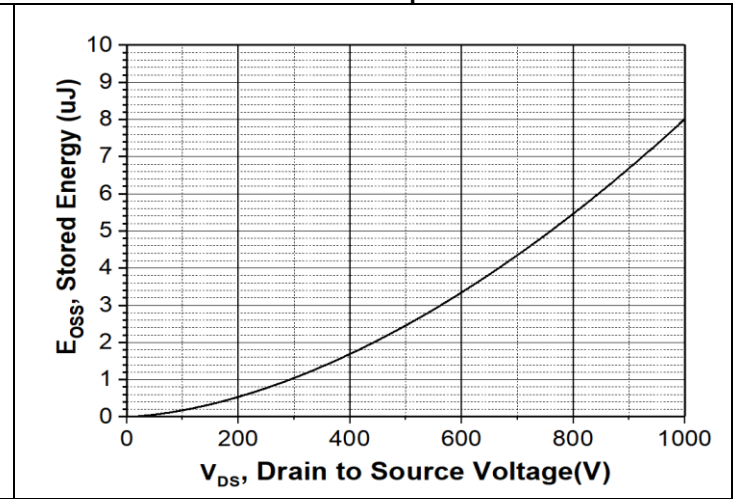


Figure 10. Output Capacitor Stored Energy

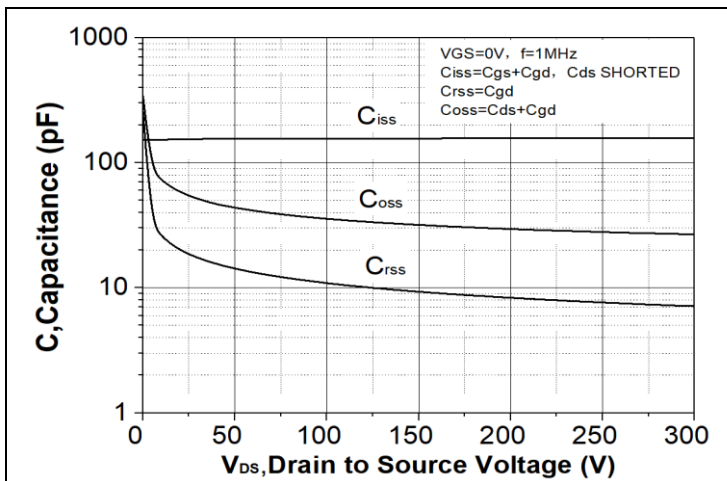


Figure 11. Capacitances vs. Drain-Source Voltage (0 - 300V)

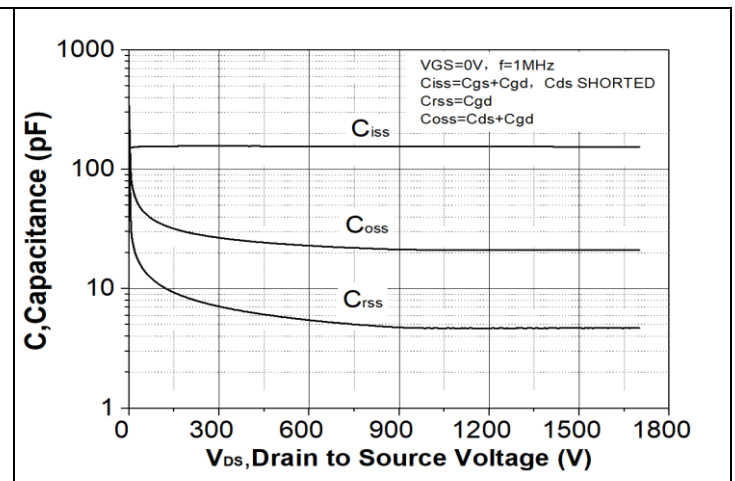


Figure 12. Capacitances vs. Drain-Source Voltage (0 - 1200V)

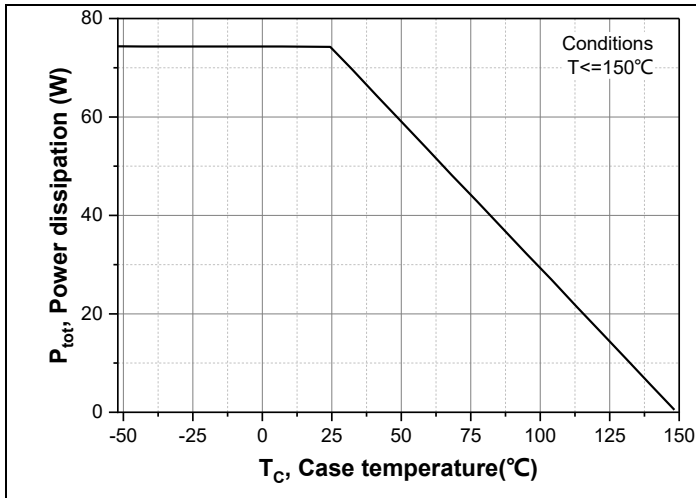


Figure 13. Maximum Power Dissipation Derating vs. Case Temperature

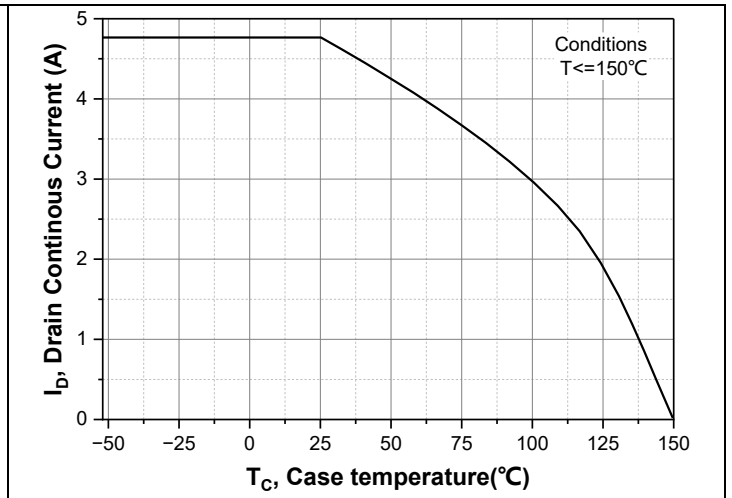


Figure 14. Continuous Drain Current Derating vs. Case Temperature

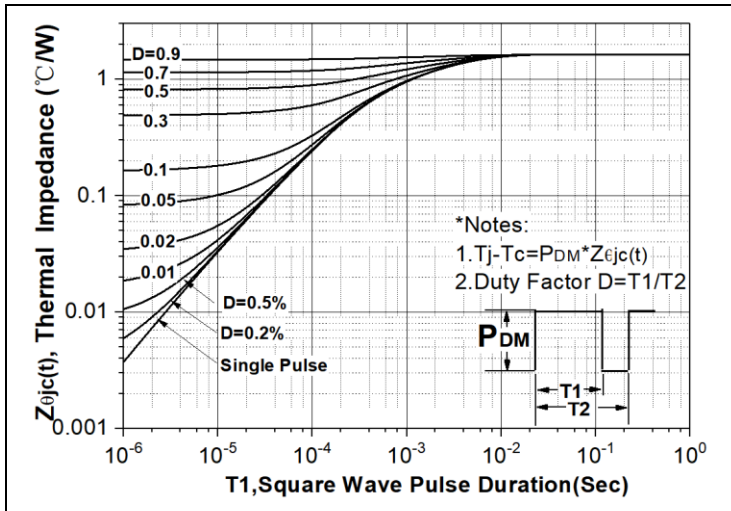


Figure 15. Transient Thermal Impedance (Junction - Case)

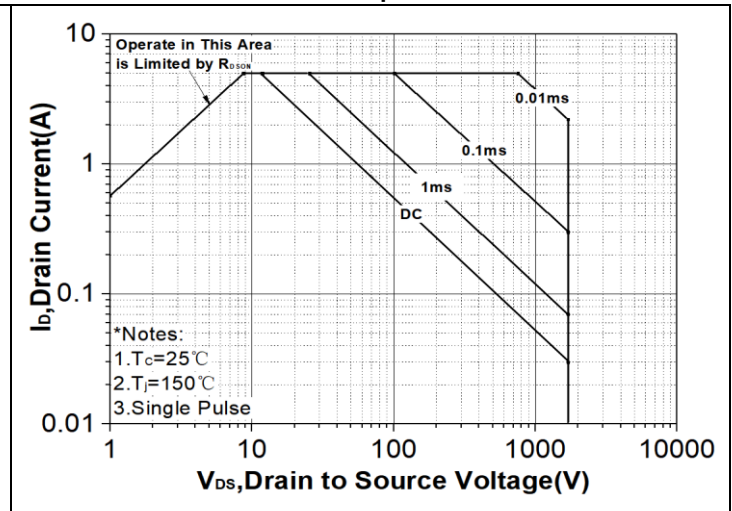


Figure 16. Safe Operating Area

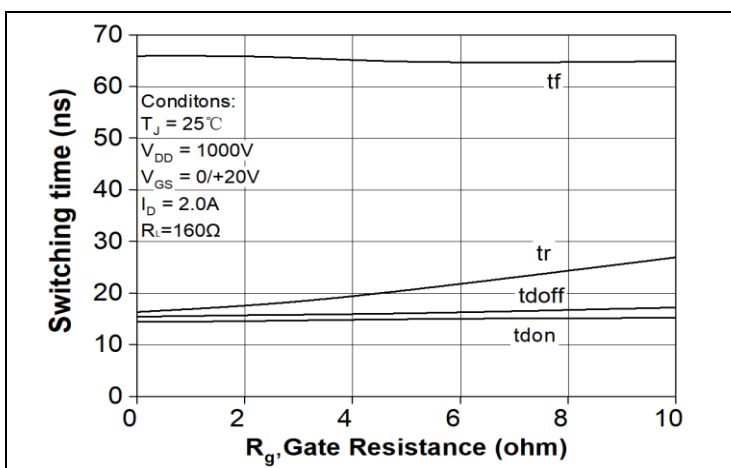
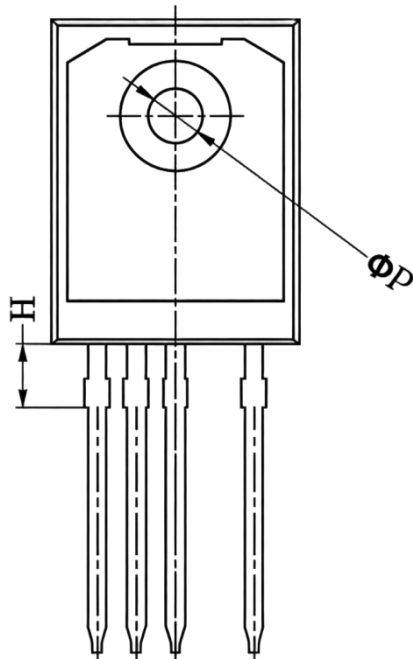
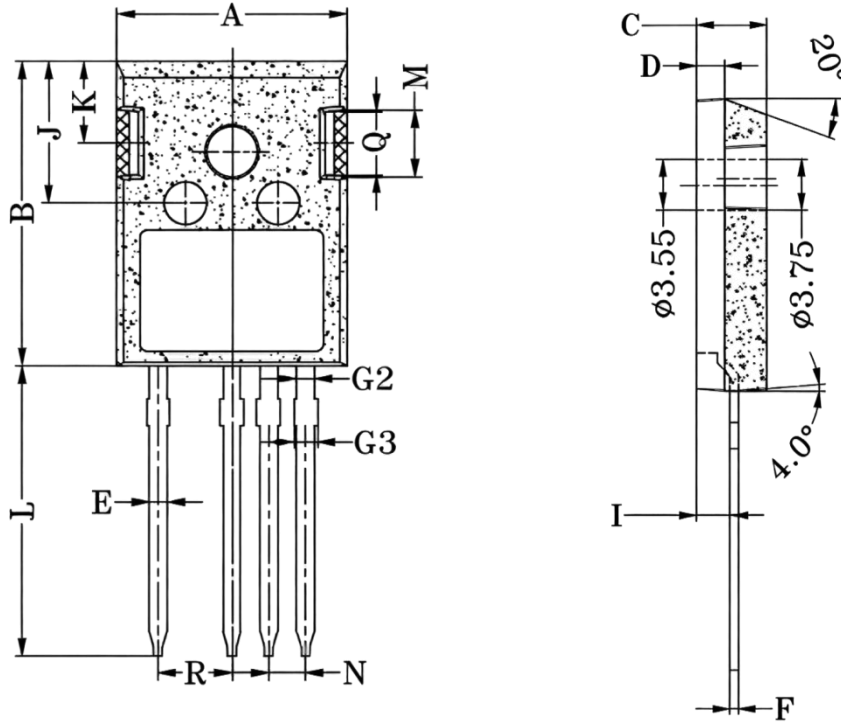


Figure 17. Resistive Switching Times vs.  $R_G(\text{ext})$

Package Outlines(Unit:mm)

TO-247-4L



Symbol	Dimensions in Millimeter	
	MIN	MAX
A	15.80	16.00
B	20.90	21.10
C	4.90	5.10
D	1.90	2.10
E	1.10	1.30
F	0.50	0.70
G2	1.10	1.30
G3	1.18	1.38
H	4.18	4.38
I	2.30	2.50
J	9.65	9.85
K	5.54	5.74
L	19.80	20.20
M	4.50	4.70
N	2.34	2.74
$\phi P$	3.40	3.60
Q	4.232	4.432
R	4.88	5.28

**\*Important Usage Information and Disclaimer**

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