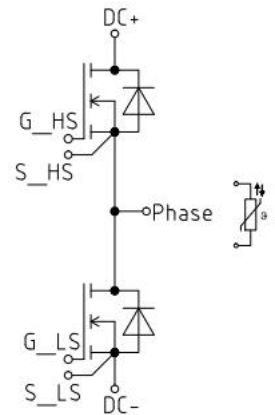


Easy1B Half Bridge SiC Module

Parameter	Value	Unit
V_{DS}	1200	V
I_D	132	A
$R_{DS(ON)}$	11.2	m Ω
Q_G	500	nC



Features:

- High Current Density
- Low Inductive Design
- Low Switching Losses
- Rugged Mounting Due to Integrated Mounting Clamps

Applications:

- DC/DC Converter
- Solar Applications
- UPS Systems
- High Frequency Switching Application

Absolute Maximum Ratings ($T_C=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Values	Unit
V_{DS}	Drain-source Voltage	1200	V
V_{GS}	Gate-source Voltage (dynamic)	-10/+22	V
I_D	Drain Current (continuous)	132	A
I_{DM}	Drain Current (pulsed)	400	A
$T_{op}; T_{stg}$	Operating and Storage Temperature Range	-40 to +150	$^\circ\text{C}$
$R_{th(j-c)}$	Thermal Resistance, Junction-to-heatsink	0.4	$^\circ\text{C}/\text{W}$
L_{Stray}	Stray Inductance	12	nH
V_{isol}	Isolation Test Voltage (DC; 2mA; t=5s)	4.2	kV
M	Mounting Force Per Clamp (M4)	20 - 50	N
W	Weight	23	g

MOSFET Characteristics

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions
Static characteristics (at $T_C=25^\circ\text{C}$ unless otherwise specified)						
B_{VDS}	Drain-source Breakdown Voltage	1200	-	-	V	$V_{GS}=0\text{V}$
I_{DSS}	Zero Gate Voltage Drain Current	-	-	200	μA	$V_{DS}=1200\text{V}; V_{GS}=0\text{V}$
I_{GSS}	Gate-body Leakage Current	-	-	4	μA	$V_{GS}=20\text{V}; V_{DS}=0\text{V}$
$V_{GS(th)}$	Gate Threshold Voltage	2.0	-	4.0	V	$V_{DS}=V_{GS}; I_D=40\text{mA}$
$R_{DS(on)}$	Static Drain-source on Resistance	-	11.2	13	m Ω	$V_{GS}=18\text{V}; I_D=80\text{A}$
$V_{GS(on)}$	Recommended Turn-on Voltage	-	18	-	V	Static
$V_{GS(off)}$	Recommended Turn-off Voltage	-	-5	-	V	

R_G	Gate Resistance	-	1.3	-	Ω	$V_{GS}=0V$; $f=1MHz$
Dynamic characteristics (at $T_C=25^\circ C$ unless otherwise specified)						
C_{iss}	Input Capacitance	-	10260	-	pF	$V_{DS}=1000V$; $f=1MHz$; $V_{AC}=25mV$
C_{oss}	Output Capacitance	-	436	-		
C_{rss}	Reverse Transfer Capacitance	-	18	-		
E_{on}	Turn-on Energy	-	8.2	-	mJ	$V_{DS}=800V$; $V_{GS}=-5/+18V$; $I_D=120A$; Load= $100\mu H$
E_{off}	Turn-off Energy	-	5.8	-		
Q_{GS}	Gate-source Charge	-	128	-	nC	$V_{DD}=800V$; $V_{GS}=-5/+18V$; $I_D=120A$
Q_{GD}	Gate-drain Charge	-	132	-		
Q_G	Total Gate Charge	-	500	-		
$t_{d(on)}$	Turn-on Delay Time	-	77	-	ns	$V_{DD}=800V$; $V_{GS}=-5/+18V$; $I_D=120A$; $R_{G(ext)}=5\Omega$; Load= $100\mu H$
t_r	Rise Time	-	55	-		
$t_{d(off)}$	Turn-off Delay Time	-	193	-		
t_f	Fall Time	-	43	-		

Body Diode Characteristics ($T_J=25^\circ C$ unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions
V_{FSD}	Forward Voltage	-	-	6	V	$V_{GS}=0V$; $I_F=120A$
I_S	Continuous Diode Forward Current	-	120	-	A	$V_{GS}=0V$; $T_C=25^\circ C$
T_{RR}	Reverse Recovery Time	-	26.6	-	ns	$V_{GS}=-5/+18V$; $I_F=120A$ $V_R=800V$
Q_{RR}	Reverse Recovery Charge	-	570	-	nC	
I_{RRM}	Peak Reverse Recovery Current	-	39.8	-	A	

NTC Thermistor Characteristics

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions
R_{25}	Rated Resistance	-	5.00	-	k Ω	$T_{NTC}=25^\circ C$
$\Delta R/R$	Deviation of R_{100}	-5	-	5	%	$T_{NTC}=100^\circ C$; $R_{100}=493.3\Omega$
$B_{25/50}$	Beta Value for $25^\circ C$ to $50^\circ C$	-	3375	-	K	$R_2=R_{25} \exp[B_{25/50}(1/T_2-1/(298.15K))]$
$B_{25/80}$	Beta Value for $25^\circ C$ to $80^\circ C$	-	3414	-	K	$R_2=R_{25} \exp[B_{25/80}(1/T_2-1/(298.15K))]$
$B_{25/100}$	Beta Value for $25^\circ C$ to $100^\circ C$	-	3436	-	K	$R_2=R_{25} \exp[B_{25/100}(1/T_2-1/(298.15K))]$

Typical Characteristics

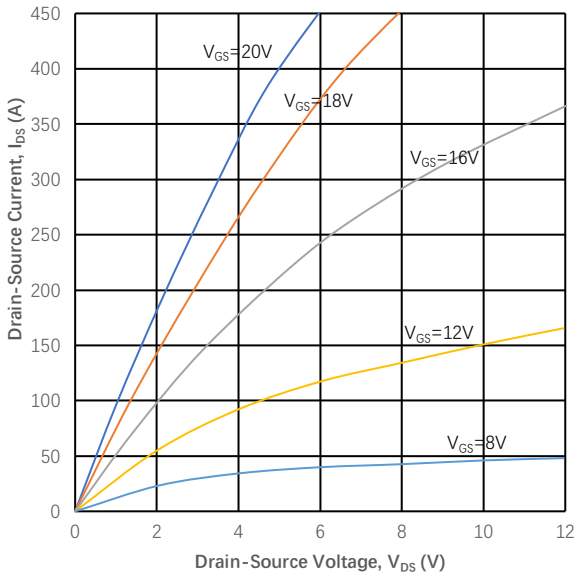


Figure 1
 Output Characteristics ($T_J=25^\circ\text{C}$)

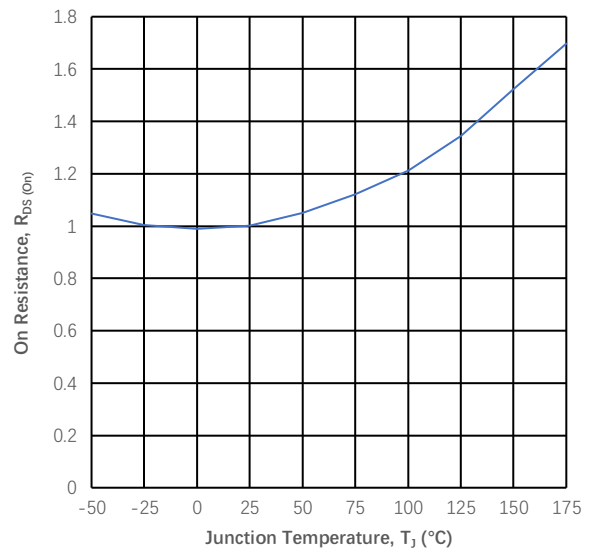


Figure 2
 Normalized on-resistance vs. Temperature

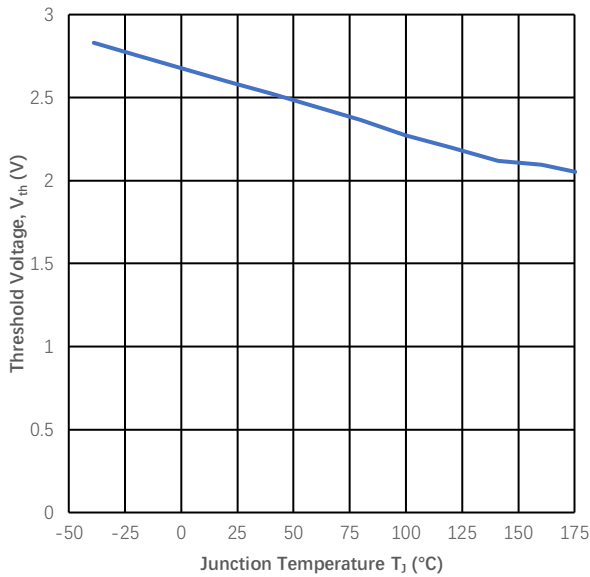


Figure 3
 Threshold Voltage vs. Temperature

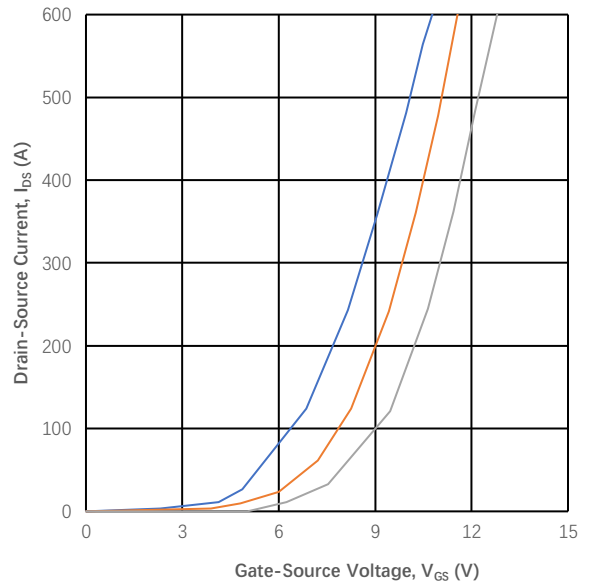


Figure 4
 Transfer Characteristic for Various T_J

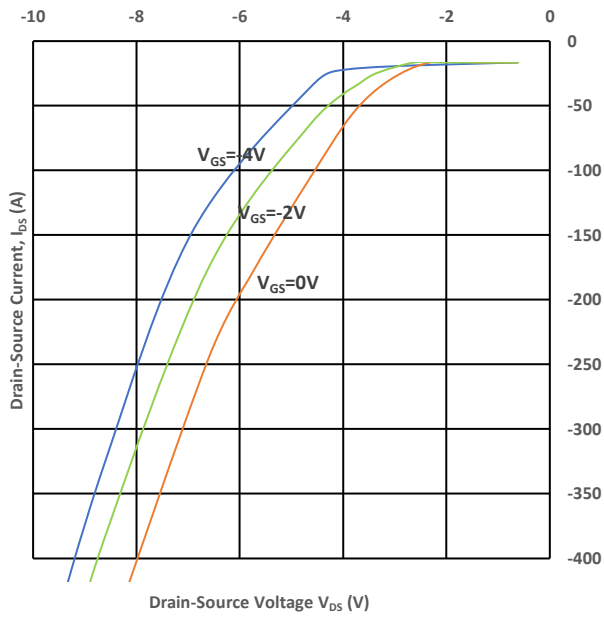


Figure 5
 Body Diode Characteristic

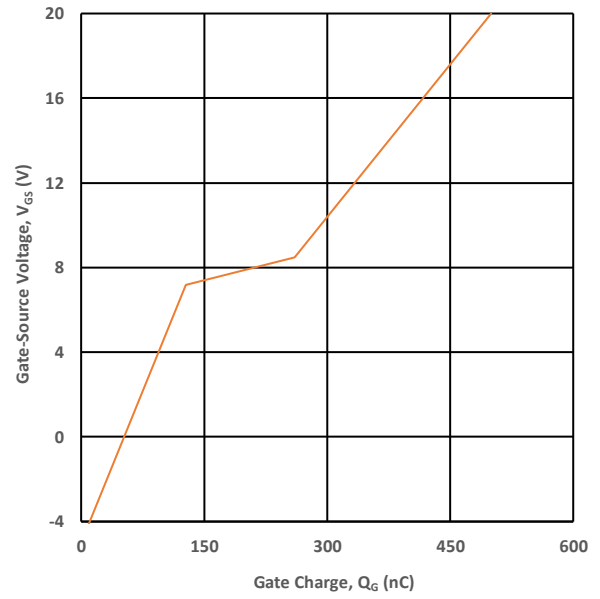


Figure 6
 Typical Gate Charge Characteristics

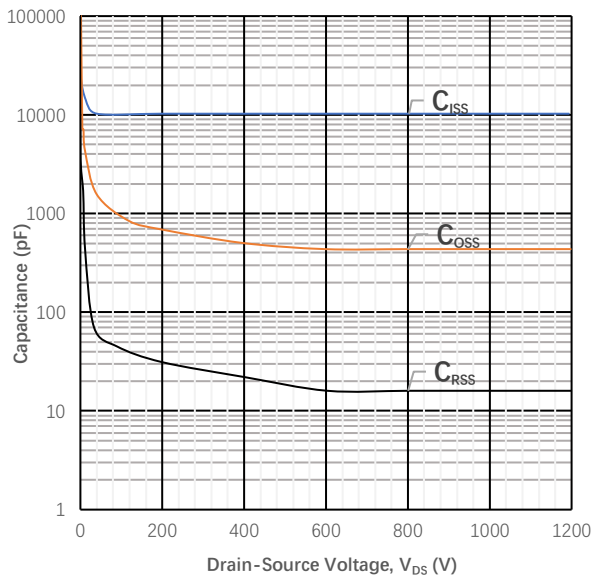


Figure 7
 Typical Capacitances vs. Drain-source Voltage

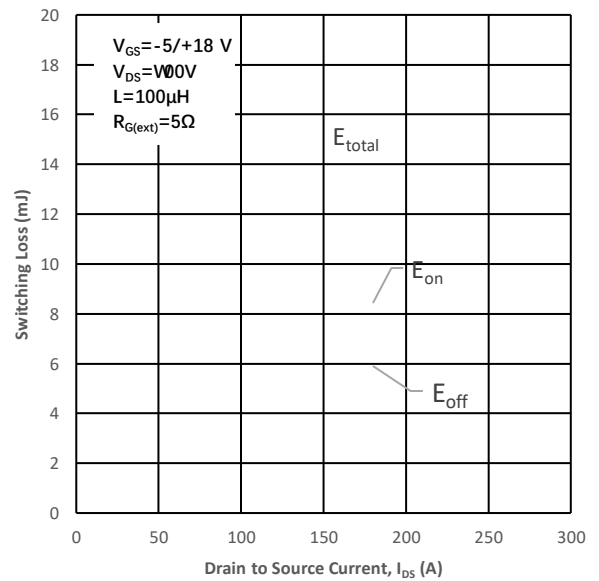


Figure 8
 Inductive Switching Energy vs. Drain Current

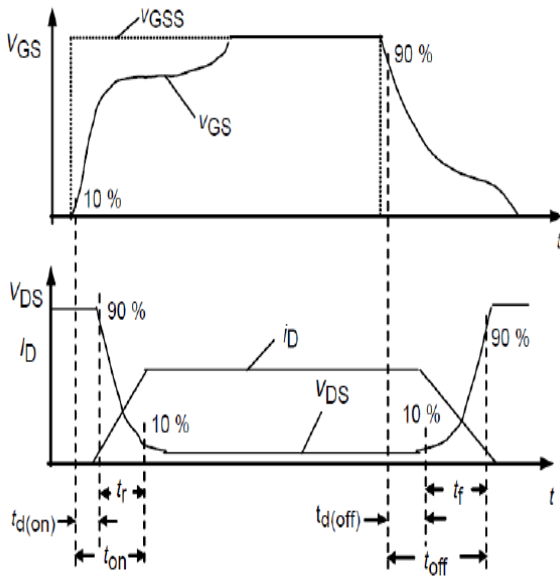


Figure 9

Inductive Switching Energy vs. Drain Current

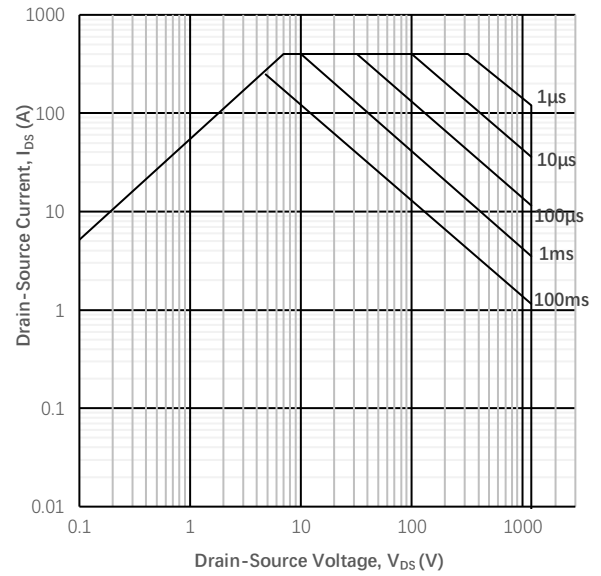
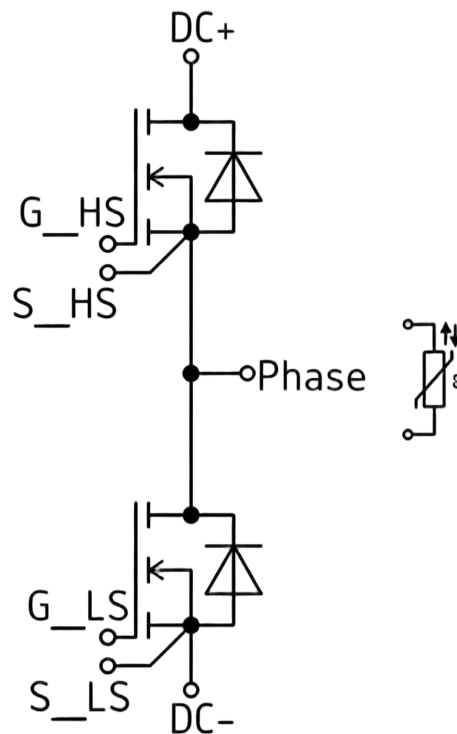


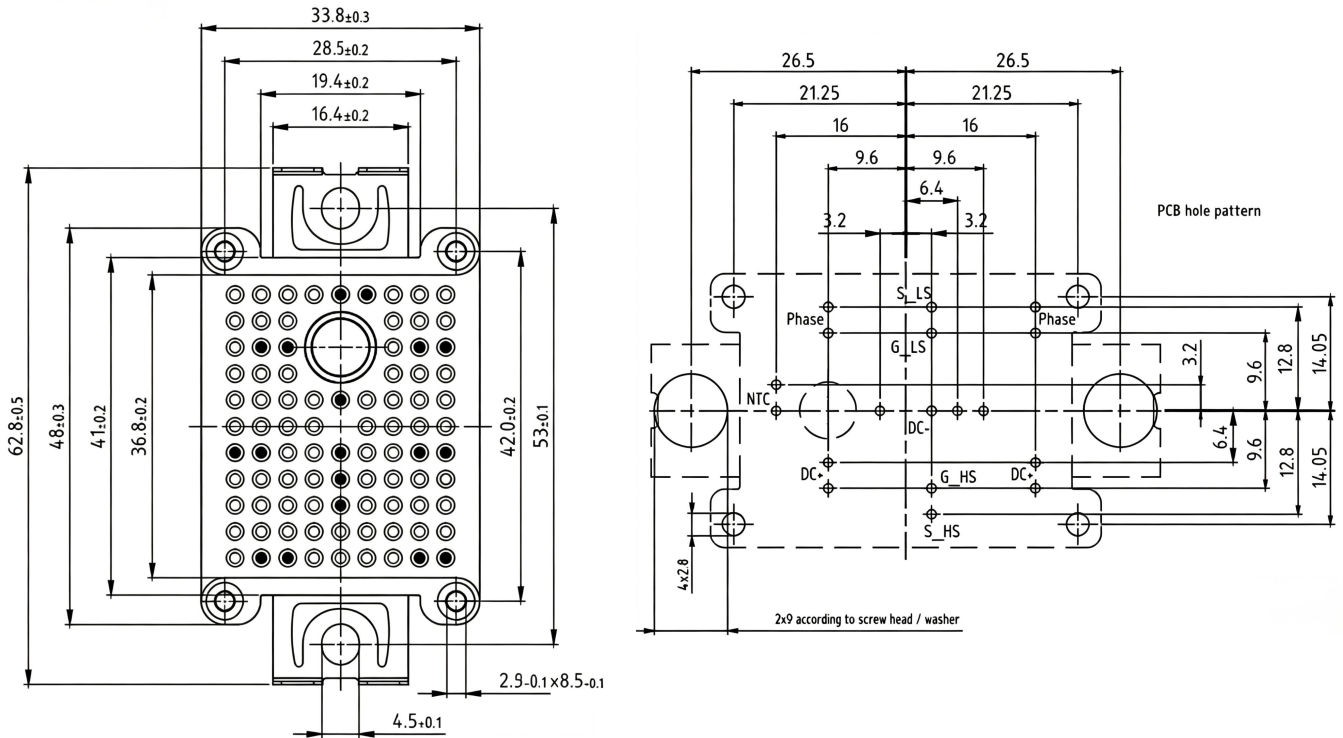
Figure 10

Safe Operating Area

Circuit Diagram



Package Outlines(Unit: mm):



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