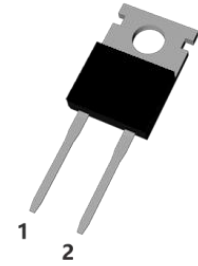
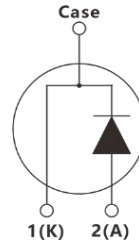


Silicon Carbide Schottky Diode

Parameter	Value	Unit
V_{RRM}	650	V
I_F	10	A
Q_C	32	nC



TO-220AC

Features

- Zero reverse recovery current
- Zero forward recovery voltage
- Temperature independent switching behavior
- High temperature operation
- High frequency operation

Applications

- Switched-Mode Power Supply
- Power Factor Correction
- Uninterruptible Power Supply
- Boost Converter

Maximum Ratings (at $T_J=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Repetitive Peak Reverse Voltage	V_{RRM}	650	V
Surge Peak Reverse Voltage	V_{RSM}	650	V
Maximum DC Blocking Voltage	V_{DC}	650	V
Continuous Forward Current $T_C = 25^\circ\text{C}$ $T_C = 135^\circ\text{C}$ $T_C = 153.5^\circ\text{C}$	I_F	30.5 14.8 10	A
Non-Repetitive Forward Surge Current $T_C = 25^\circ\text{C}, t_p = 8.3\text{ms}, \text{Half Sine Pulse}$	I_{FSM}	100	A
Power dissipation $T_C = 25^\circ\text{C}$ $T_C = 110^\circ\text{C}$	P_{tot}	115 50	W
Operating junction Range	T_j	-55 to +175	$^\circ\text{C}$
Storage temperature Range	T_{stg}	-55 to +175	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	Typ.	Unit
Thermal resistance, junction – case.	R_{thJC}	1.3	$^{\circ}C/W$

Electrical Characteristics(at $T_J=25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Instantaneous forward voltage per leg	V_F	$I_F=10A, T_J=25^{\circ}C$ $I_F=10A, T_J=175^{\circ}C$		1.39 1.66	1.7 2.0	V
Reverse current per leg	I_R	$V_R=650V, T_J=25^{\circ}C$ $V_R=650V, T_J=175^{\circ}C$		0.35 2.4	50 100	μA
Total Capacitance	C	$V_R=0V, T_J=25^{\circ}C$ $f=1MHz$		617		pF
Total Capacitive Charge	Q_C	$V_R=400V, I_F=10A$ $di/dt=200A/us$ $T_J=25^{\circ}C$		32		nC

Typical Characteristics

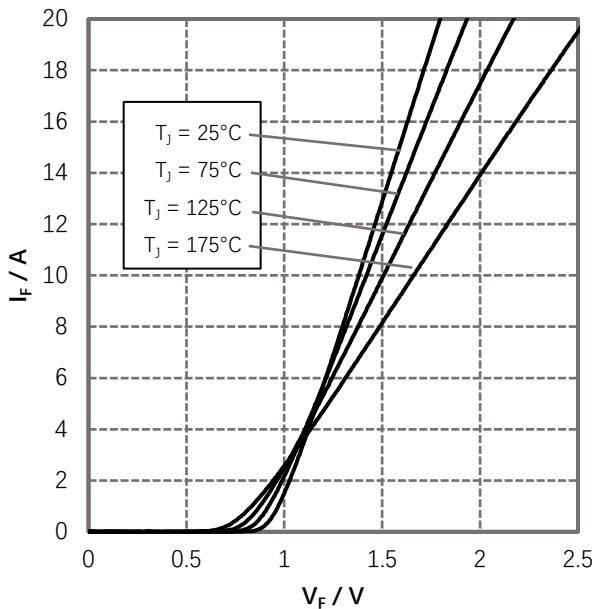


Fig 1. Typical forward characteristics

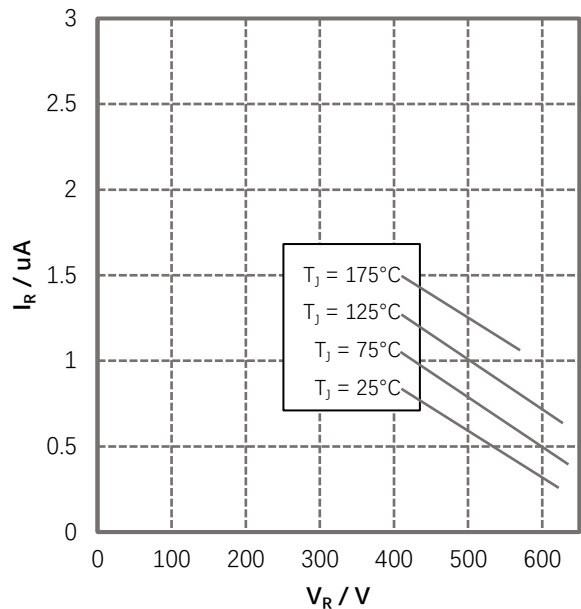


Fig 2. Typical reverse current as function of reverse voltage

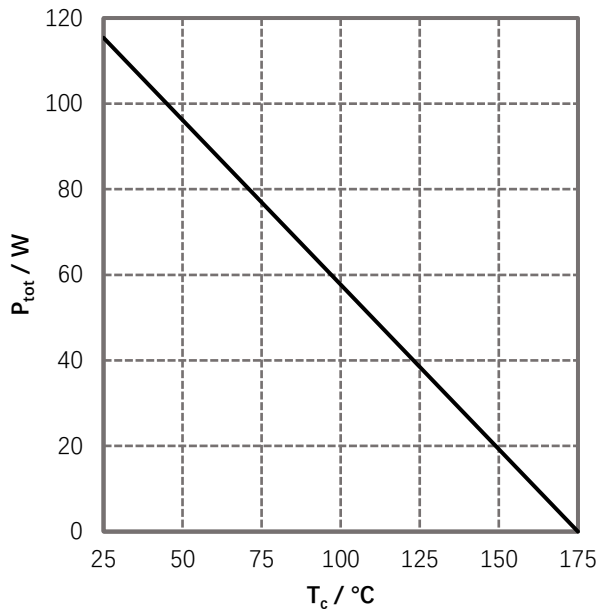


Fig 3. Power Derating

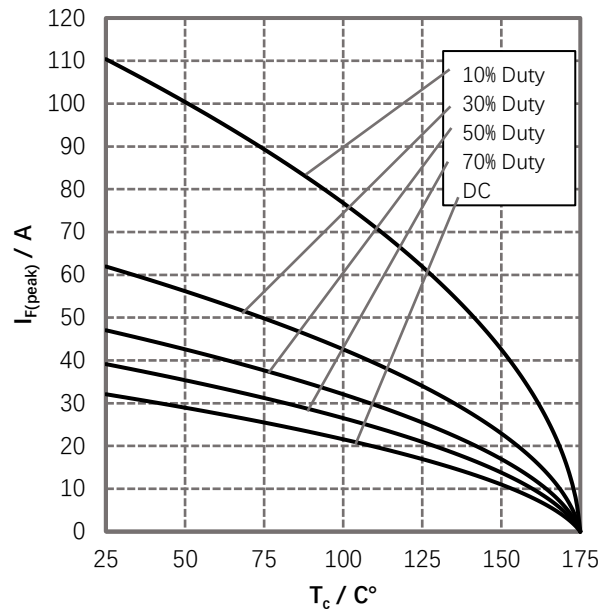


Fig 4. Current Derating

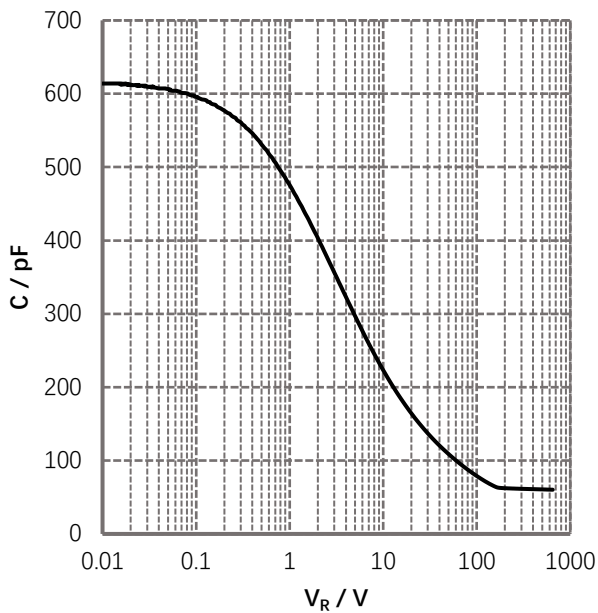


Fig 5. Typical capacitance as function of reverse voltage, $C=f(V_R)$; $T_j=25^\circ\text{C}$; $f=1 \text{ MHz}$

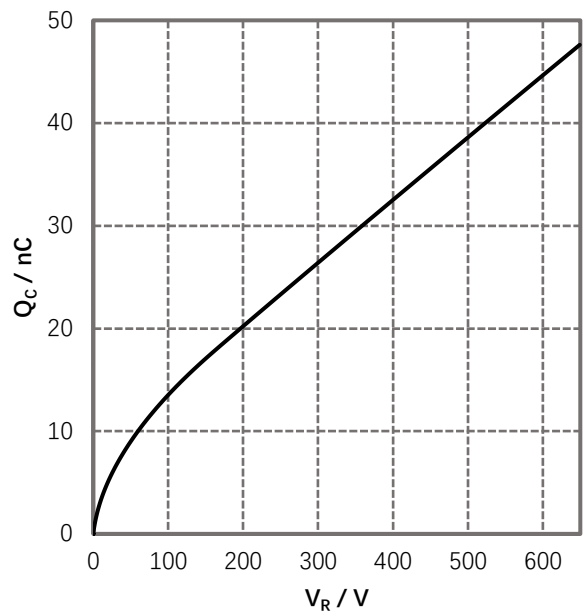


Fig 6. Typical reverse charge as function of reverse voltage

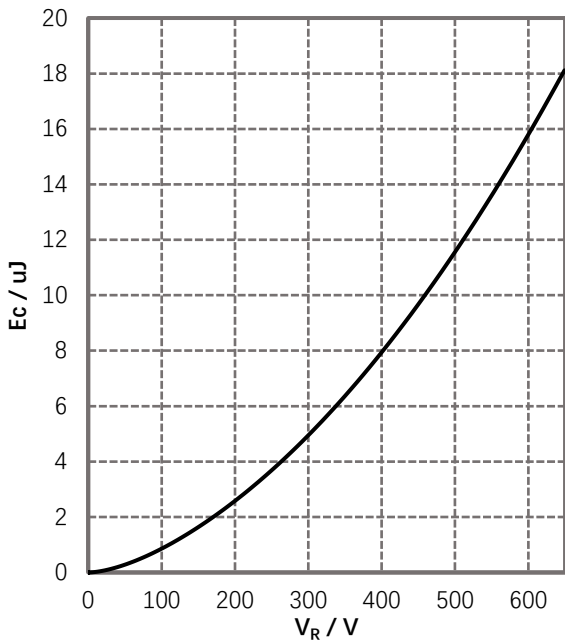


Fig 7. Capacitance Stored Energy

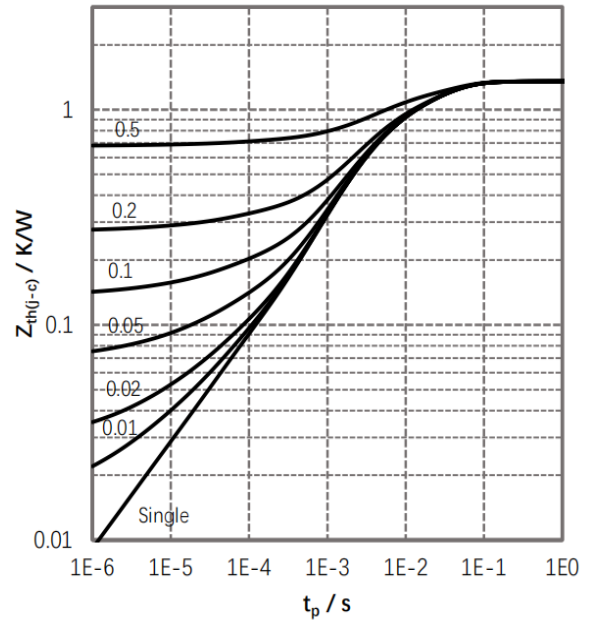
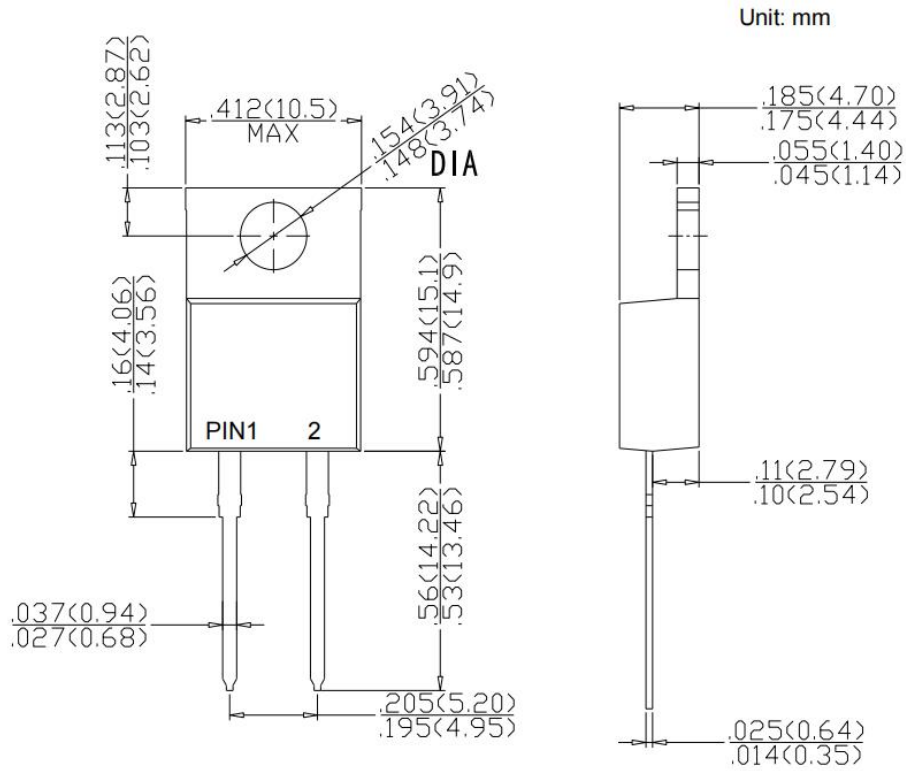


Fig 8. Transient Thermal Impedance

Package Outlines(Unit:mm)

TO-220AC



***Important Usage Information and Disclaimer**

The specifications of Zhuhai Hypersemi Co., Ltd. products are not guarantees of product characteristics. They reflect typical performance expected in standard applications, which may vary with specific uses. Users must conduct prior testing for their applications and make necessary adjustments.

Users are responsible for the safety of applications utilizing our products and must implement adequate safety measures to prevent physical injury, fire, or other risks in case of product failure. It is the user's duty to ensure that application designs comply with all applicable laws and standards. Our products must not be used in any applications where a product failure could reasonably result in personal injury, unless specifically authorized in a signed document by Zhuhai Hypersemi Co., Ltd.

No representations or warranties are made regarding the accuracy or completeness of this information, including any claims of non-infringement of third-party intellectual property rights. Zhuhai Hypersemi Co., Ltd. assumes no liability for any applications or uses of its products and does not grant any licenses to its intellectual property rights or those of others. We also make no claims regarding non-infringement of third-party intellectual property rights that may arise from applications.

Due to technical requirements, our products may contain hazardous substances. For details, please contact your nearest sales office. This document replaces all previous information and may be updated. We reserve the right to make changes.