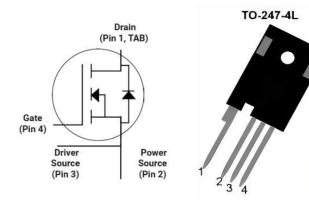


## SiC MOSFET N-channel 165A/1200V

Parameter	Value	Unit
BVpss	1200	V
RDS(ON),typ.(20V)	13	mΩ
V <sub>GS(TH)</sub>	2.5 ~ 3.5	V
Eon	9	mJ
Eoff	2.1	mJ
ID (at TC=25℃)	165	Α



### **FEATURES**

- High Speed Switching with Low Capacitance
- High Blocking Voltage with Low On-Resistance
- Easy to Parallel and Simple to Drive
- Avalanche Ruggedness
- Reduced Cooling Requirements

# **APPLICATIONS**

- Solar Inverters
- Switch Mode Power Supplies
- High Voltage DC/DC Converters
- Battery Chargers
- Pulsed Power Applications

# MAXIMUM RATED VALUES (at TJ = 25 °C, unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions
V <sub>DSmax</sub>	Drain-Source Voltage	1200	V	$V_{GS} = 0 V$ , $I_D = 1mA$
V <sub>GSmax</sub>	Gate-Source Voltage	-10/+25	V	Absolute maximum values
V <sub>GSop</sub>	Gate-Source Voltage	-5/+18	V	Recommended operational values
L	Continuous Drain Current 165		Α	V <sub>GS</sub> = 18 V, Tc = 25°C
l <sub>D</sub>	Continuous Diani Current	150	Α	V <sub>GS</sub> = 18 V, Tc = 60°C
I <sub>D(pulse)</sub>	Pulsed Drain Current	300	Α	Pulse width t <sub>P</sub> limited by T <sub>jmax</sub>
P <sub>D</sub>	Power Dissipation	288	W	T <sub>C</sub> =25°C, T <sub>J</sub> = 175°C
$T_{J},T_{stg}$	Operating Junction and Storage Temperature	-40 to +175	ç	
T <sub>L</sub>	Solder Temperature	260	Ç	1.6mm (0.063") from case for 10s

#### THERMAL CHARACTERISTICS

Symbol	Parameter	Min.	Тур.	Max.	Unit
ReJC	Thermal Resistance, Junction-to-Case			0.27	°C/W
Reja	Thermal Resistance, Junction-to-Ambient			26.8	C/VV

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# **ELECTRICAL CHARACTERISTICS** (at TJ = 25°C unless otherwise specified)

Symbol	Parameter	Min.	Тур.	Max.	Unit	Test Conditions	
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	1200	-	-	V	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 1mA	
		2.5	3.0	3.5	V	$V_{DS} = V_{GS}$ , $I_D = 10mA$	
$V_{\text{GS(th)}}$	Gate threshold Voltage	-	2.5	-	V	$V_{DS} = V_{GS}, I_{D} = 10 \text{mA}, T_{J} = 175 ^{\circ}\text{C}$	
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	-	-	50	μΑ	V <sub>DS</sub> = 1200 V, V <sub>GS</sub> = 0 V	
$I_{\rm GSS}$	Gate Source Leakage Current	-	-	200	nA	V <sub>GS</sub> = 18 V, V <sub>DS</sub> = 0 V	
_	Drain-Source	-	13	16		$V_{GS} = 18 \text{ V}, I_{D} = 100 \text{ A}$	
R <sub>DSON</sub>	On-State Resistance	-	24	-	mΩ	$V_{GS} = 18 \text{ V}, I_{D} = 100 \text{A}, $ $T_{J} = 175 ^{\circ} \text{C}$	
a	Transconductance	-	58	-	S	V <sub>GS</sub> = 20 V, I <sub>D</sub> = 100A	
$g_{fs}$	Transconductance	-	55	-	3	V <sub>GS</sub> = 20 V, I <sub>D</sub> = 100A, T <sub>J</sub> =175°C	
$C_{iss}$	Input Capacitance	-	7500	-		V <sub>DS</sub> =800V,V <sub>GS</sub> =0V, T <sub>J</sub> =25°C,f=100kHz	
C <sub>oss</sub>	Output Capacitance	-	284	-	pF		
C <sub>rss</sub>	Reverse Capacitance	-	18	-	]		
E <sub>oss</sub>	C <sub>oss</sub> Stored Energy	-	161	-	μJ		
E <sub>on</sub>	Turn on Switching Energy	-	9000	-		V <sub>DS</sub> = 800 V, V <sub>GS</sub> = -5/+15 V,	
E <sub>off</sub>	Turn off Switching Energy	-	2100	-	μJ	$I_D=150A, R_{g(ext)}=5\Omega, T_J=125^{\circ}C$	
t <sub>don</sub>	Turn on delay time	-	31	-			
t <sub>r</sub>	Rise time	-	47	-	]	$V_{DS} = 800 \text{ V}, V_{GS} = -5/+15 \text{ V}$	
t <sub>doff</sub>	Turn off delay time	-	83	-	ns	$I_D = 150A, R_{g(ext)} = 5\Omega, T_J = 125^{\circ}C$	
t <sub>f</sub>	Fall time	-	76	-	]		
$R_{gint}$	Internal Gate Resistance	-	2.5	-	Ω	V <sub>AC</sub> =25mV, f= 1MHz	
$Q_{gs}$	Gate to Source Charge	-	70	-			
$Q_{gd}$	Gate to Drain Charge	-	92	-	nC	$V_{DS} = 800 \text{ V}, V_{GS} = -5/+18 \text{ V},$	
$Q_g$	Total Gate Charge	-	260	-	]	I <sub>D</sub> = 150A	

## **SOURCE-DRAIN BODY DIODE CHARACTERISTICS**

Symbol	Parameter	Min	Тур.	Max.	Unit	Test Conditions	
IsD	Continuous Source Current			132	Α	V <sub>GS</sub> = -5 V, Tc=25°C	
\/	Diada Fanyard\/altara		4.9		V	V <sub>GS</sub> = -5 V, I <sub>SD</sub> = 100 A	
VsD	/sp Diode Forward Voltage		4.5			V <sub>GS</sub> = -5 V, I <sub>SD</sub> = 100 A,T <sub>J</sub> = 175°C	
t <sub>rr</sub>	Reverse Recovery Time		67		ns	$V_R$ = 800 V, $V_{GS}$ = -5V/+15V, $I_D$ = 150A, di/dt=2500A/μS, $T_J$ = 125°C, $R_G$ =5 Ω	
Qrr	Reverse Recovery Charge		1600		nC		
Irrm	Peak Reverse Recovery Current		36		Α		

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#### TYPICAL CHARACTERISTICS CURVES

Figure 1. Output Characteristics TJ = -40 °C

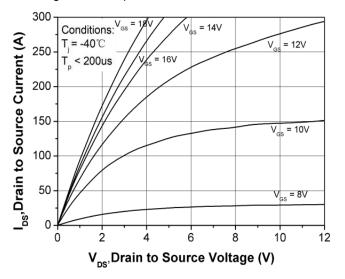


Figure 3. Output Characteristics TJ = 175°C

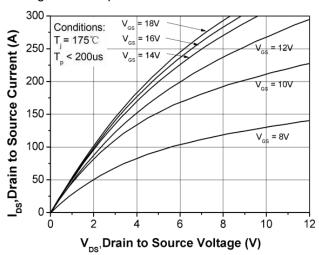


Figure 5. On-Resistance vs. Drain Current For Various Temperatures

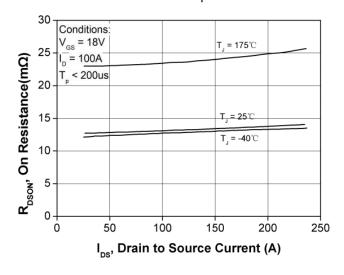


Figure 2. Output Characteristics TJ = 25 °C

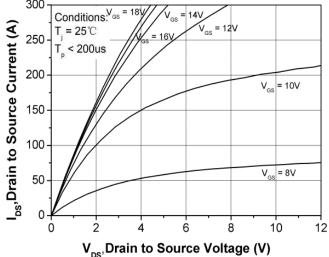


Figure 4. Normalized On-Resistance vs. Temperature

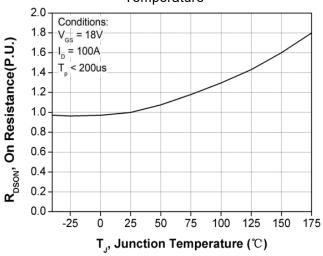
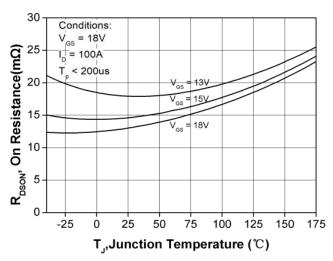


Figure 6. On-Resistance vs. Temperature For Various Gate Voltage



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Figure 7. Transfer Characteristic for Various Junction Temperatures

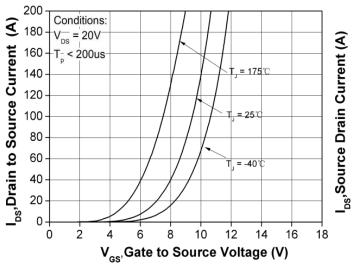


Figure 8. Body Diode Characteristic at TJ = -40°C

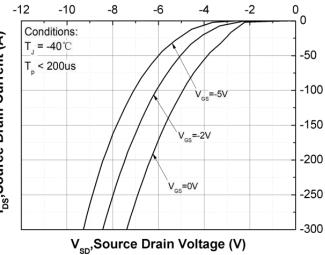
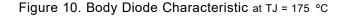
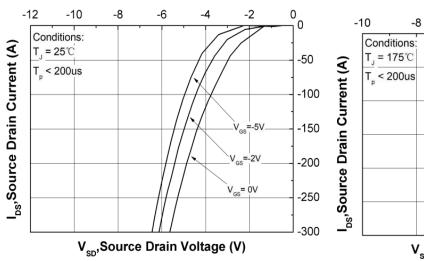


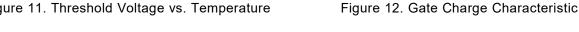
Figure 9. Body Diode Characteristic at TJ = 25 °C

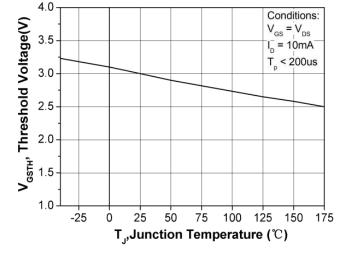


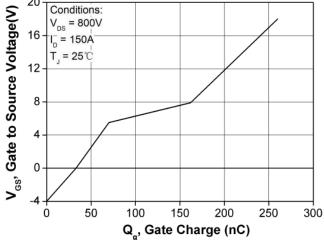


0 -2 -6 -50 -100 -150 V<sub>gs</sub>= 0V -200 -250 -300 V<sub>sp</sub>,Source Drain Voltage (V)

Figure 11. Threshold Voltage vs. Temperature







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Figure 13. 3rd Quadrant Characteristic at TJ = -40 °C

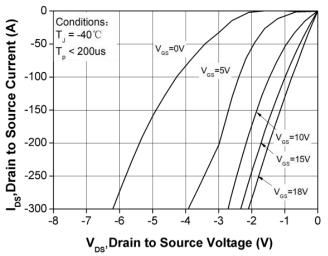


Figure 14. 3rd Quadrant Characteristic at TJ = 25 °C

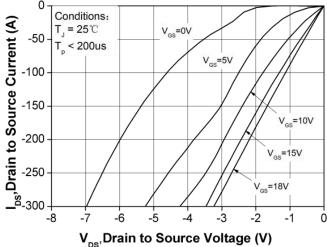


Figure 15. 3rd Quadrant Characteristic at TJ = 175°C

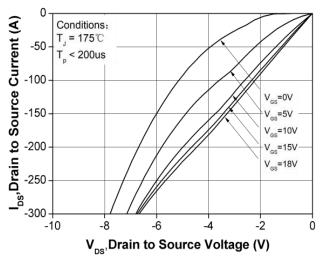


Figure 16. Output Capacitor Stored Energy

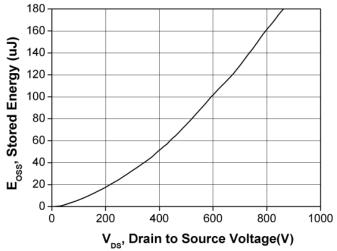


Figure 17. Capacitance vs. Drain-Source Voltage (0 - 200V)

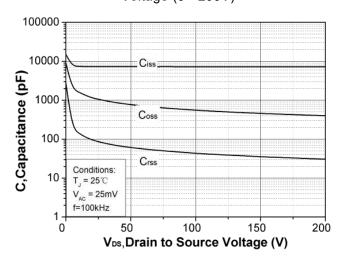
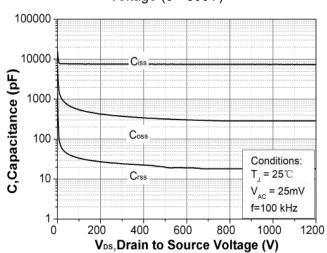


Figure 18. Capacitance vs. Drain-Source Voltage (0 - 800V)



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Figure 20. Maximum Power Dissipation Derating

vs.Case Temperature

Conditions

T, ≤ 175°C

125

150

175

Figure 19. Continuous Drain Current Derating vs. Case Temperature

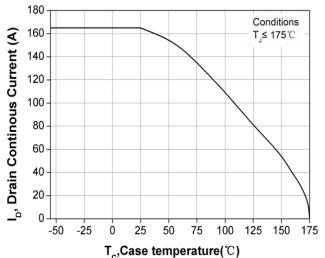
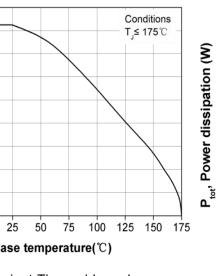


Figure 21. Transient Thermal Impedance (Junction - Case)



300

250

200

150

100

50

n

-50

-25

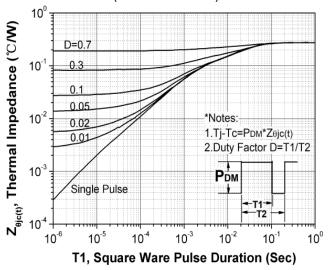


Figure 23. Clamped Inductive Switching Energy vs. Low Drain Current (VDD= 800V)

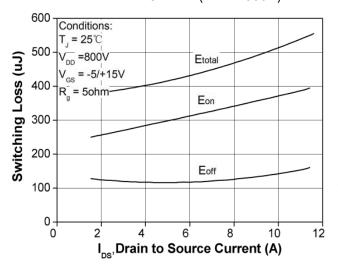


Figure 22. Safe Operating Area

50

T<sub>c</sub>,Case temperature(°C)

75

100

25

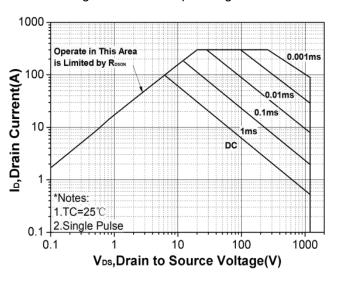
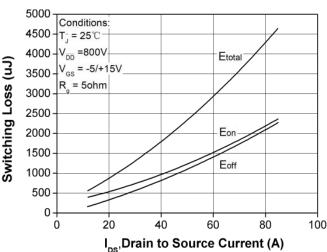


Figure 24. Clamped Inductive Switching Energy vs. High Drain Current (VDD= 800V)



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Figure 25. Clamped Inductive Switching Energy vs. RG(ext)

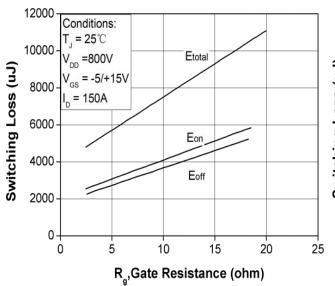


Figure 26. Clamped Inductive Switching Energy vs. Temperature

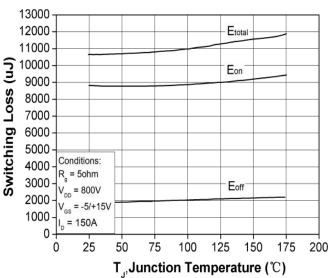


Figure 27. Switching Times vs. RG(ext)

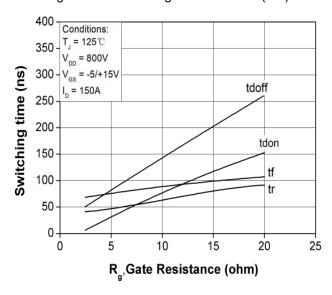
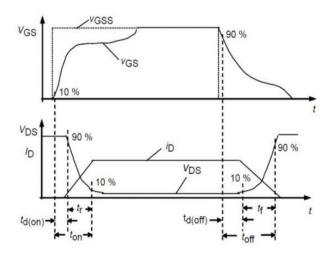


Figure 28. Switching Times Definition

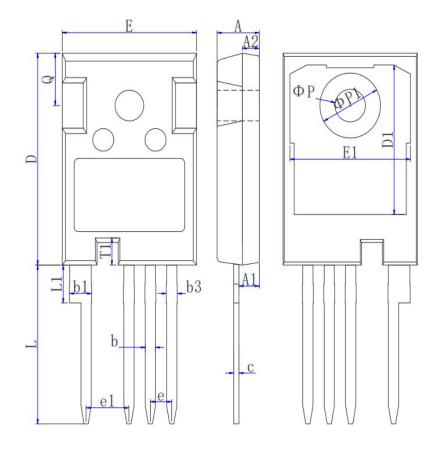


#### **PACKAGE OUTLINES**

TO-247-4L

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SYMBOL	ММ						
	MIN	NOM	MAX				
Α	4.80	5.00	5.20				
<b>A</b> 1	2.21	2.41	2.61				
A2	1.80	2.00	2.20				
b	1.06	1.21	1.36				
b1	2.33	2.63	2.93				
b3	1.07	1.30	1.60				
С	0.51	0.61	0.75				
D	23.30	23.45	23.60				
D1	16.25	16.55	16.85				
E	15.74	15.94	16.14				
E1	13.72	14.02	14.32				
T1	2.35	2.50	2.65				
е	2.54BSC						
e1	5.08BSC						
Q	5.49	5.79	6.09				
L	17.27	17.57	17.87				
L1	3.99	4.19	4.39				
ФР	3.40	3.60 3.80					
ФР1	7.19REF						

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# CM165N120L4X100 N-channel Silicon Carbide MOSFET

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