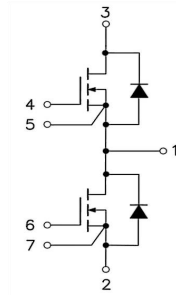


34mm SiC Half Bridge Module

Parameter	Value	Unit
V _{CES}	1200	V
I _C	126	A
R _{DS(ON)}	15	mΩ



Features:

- Low switching losses
- Low inductance design
- Fast intrinsic diode with low reverse recovery

Applications:

- DC/DC, High-frequency switch applications
- Motor drive, Power supply
- Inverter, Welding applications

Maximum Ratings (SiC MOSFET T_J=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Ratings	Unit
V _{DSS}	Drain-Source Voltage	G-S Short	1200	V
V _{GSS}	Gate-Source Voltage	D-S Short, AC frequency ≥1Hz, Note1	-10 to 23	V
I _{DS}	DC Continuous Drain Current	T _C =25°C, V _{GS} =18V	126	A
		T _C =100°C, V _{GS} =18V	90	A
I _{SD}	Source-Drain Current(diode)	T _C =25°C, with ON signal	126	A
		T _C =100°C, with ON signal	90	A
I _{DSM}	Pulse Drain Current	T _C =25°C, Pulse width =1ms, V _{GS} =18V, Note2	340	A
P _{tot}	Total Power Dissipation	T _C =25°C	893	W
T _{jmax}	Max Junction Temperature	-	175	°C
T _{stg}	Storage Temperature	-	-55 to 175	°C

Note1: Recommended Operating Value, +18V/-4V

Note2: Pulse width limited by maximum junction temperature

SiC MOSFET Electrical characteristics (T_J =25°C unless otherwise specified, chip)

Symbol	Item	Condition	Value			Unit
			Min.	Typ.	Max	
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =100μA	1200	-	-	V
V _{GS(th)}	Gate-source threshold Voltage	I _D =27mA, V _{DS} =V _{GS}	T _J =25°C	2.0	2.8	3.7
			T _J =150°C	-	2.1	-
			T _J =175°C	-	2.0	-
I _{DSS}	Zero gate voltage drain Current	V _{DS} =1200V, V _{GS} =0V	T _J =25°C	0	1	50
I _{GSS}	Gate-Source Leakage Current	V _{GS} =18V, V _{DS} =0V	T _J =25°C	0	1	200
R _{DS(on)}	Static drain-source	I _D =80A	T _J =25°C	-	15	18
			T _J =150°C	-	25	-

(Chip)	On-state resistance	$V_{GS}=+18V$	$T_j=175^{\circ}C$	-	28	-	mΩ
$V_{DS(on)}$	Static drain-source	$I_D=80A$	$T_j=25^{\circ}C$	-	1.89	-	V
(Chip)	On-state Voltage	$V_{GS}=+18V$	$T_j=150^{\circ}C$		3.15		
			$T_j=175^{\circ}C$	-	3.53	-	
C_{iss}	Input Capacitance	$V_D=1000V, V_{GS}=0V$ $f=1MHz, V_{AC}=25mV$		-	4.3	-	nF
C_{oss}	Output Capacitance			-	0.214	-	
C_{rss}	Reverse transfer Capacitance			-	0.019	-	
R_{Gint}	Internal gate resistor	$f=1MHz, I_D=0V$		-	1.4	-	Ω
Q_g	Total gate charge	$V_{DD}=800V,$ $I_D=80A,$ $V_{GS}=+18/-4V$		-	222	-	nC
Q_{gs}	Gate-source charge			-	55	-	
Q_{gd}	Gate-drain charge			-	88	-	
$R_{th(j-c)}$	FET Thermal Resistance	Junction to Case, Note1		-	0.168	-	$^{\circ}C/W$

Note1: Assumes Thermal Conductivity of grease is 2.8 W/m · K and thickness is 50μm.

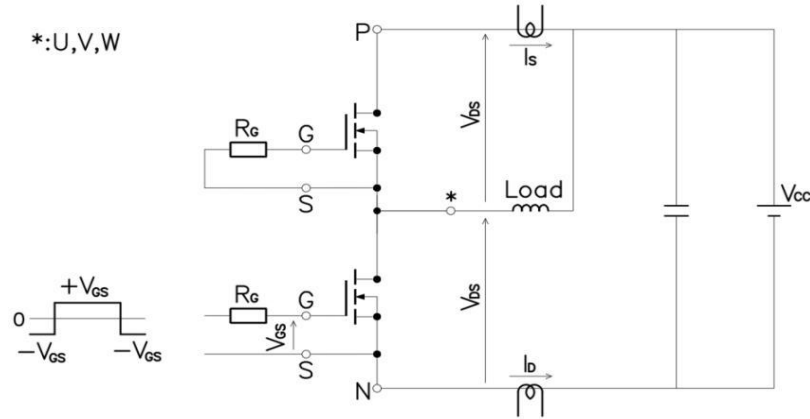
Body Diode Electrical characteristics ($T_j=25^{\circ}C$ unless otherwise specified, chip: Target)

Symbol	Item	Condition		Value			Unit
				Min.	Typ.	Max	
V _{SD}	Body Diode Forward Voltage	V _{GS} =-4V, I _{SD} =40A	T _j =25°C	-	4.1	-	V
			T _j =150°C	-	3.7	-	
			T _j =175°C	-	3.6	-	
I _S	Continuous Diode Forward Current	V _{GS} = -4V	T _j =25°C	-	-	97	A
T _{rr}	Reverse recovery time	V _{DD} =800V, I _D =80A V _{GS} =+18/-4V, R _g =1Ω Inductive load switching operation	T _j =25°C	-	21	-	ns
Q _{rr}	Reverse recovery charge		T _j =25°C	-	470	-	nC
I _{rr}	Diode switching power dissipation		T _j =25°C	-	40	-	A

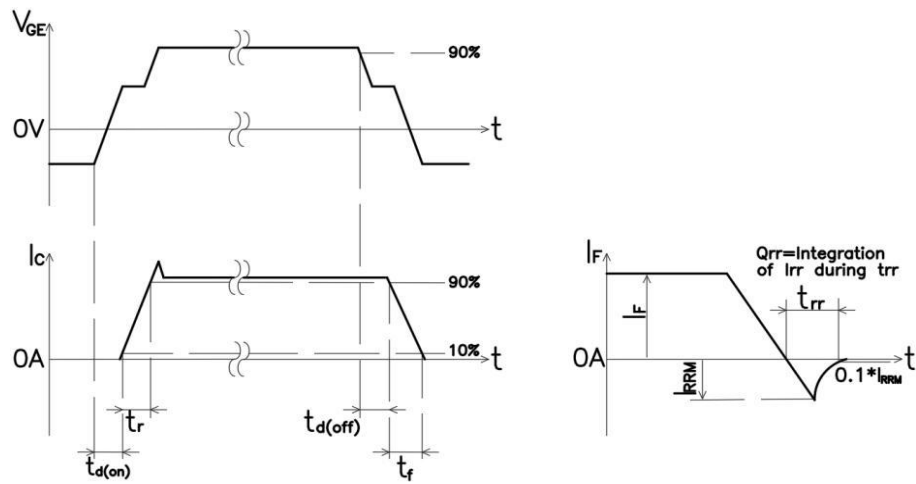
Module Characteristics

Parameter	Conditions	Value	Unit
Isolation Voltage	RMS, $f=50Hz, t=1min$	2.5	kV
Material of module baseplate	-	Cu	-
Creepage distance	terminal to heatsink terminal to terminal	26 21	mm
Clearance	terminal to heatsink terminal to terminal	23.6 10	mm
CTI	-	>200	-
Module lead resistance, terminals - chip	$T_c=25^{\circ}C$	0.8	mΩ
Mounting torque for module mounting	M5, M6	3 to 6	Nm
Weight	-	160	g

Test Conditions



Switching time measure circuit



Switching time definition

Characteristics diagrams

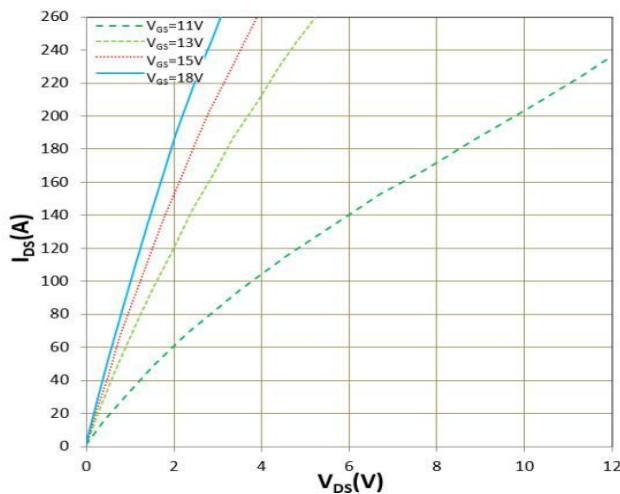


Figure 1. I_{DS} vs V_{DS} ($T_{vj}=25^{\circ}\text{C}$)

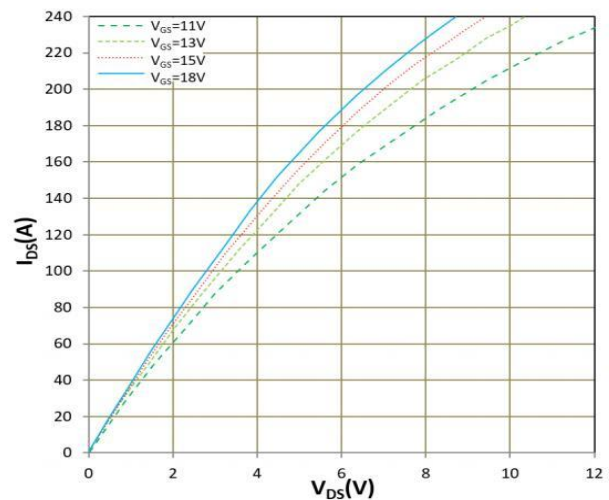


Figure 2. I_{DS} vs V_{DS} ($T_{vj}= 175^{\circ}\text{C}$)

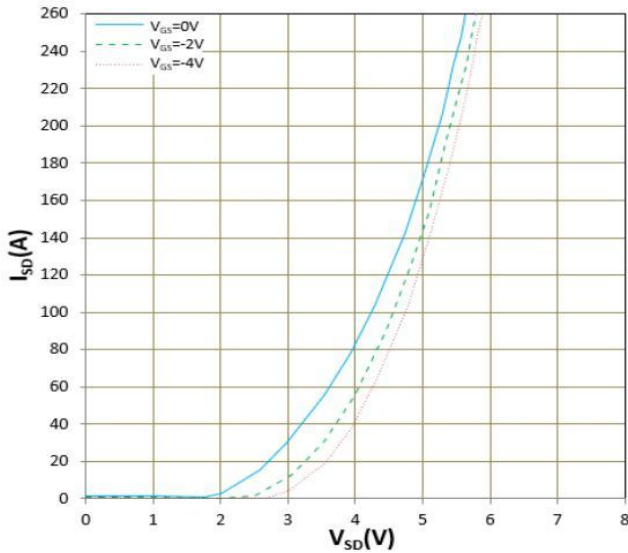


Figure 3. ISD vs VSD (Tvj= 125°C)

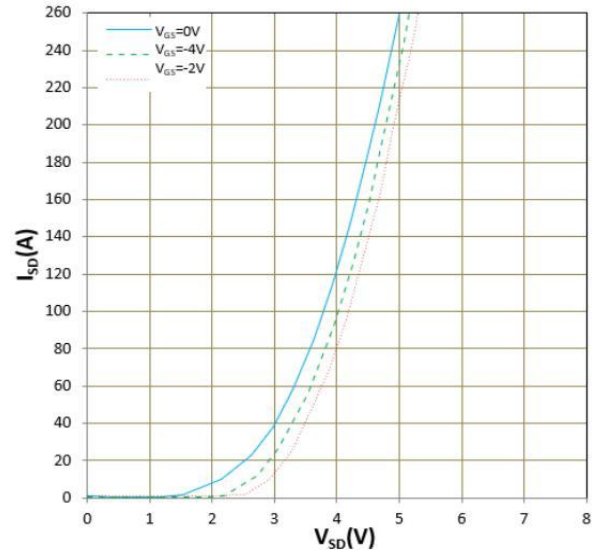


Figure 4. ISD vs VSD (Tvj= 175°C)

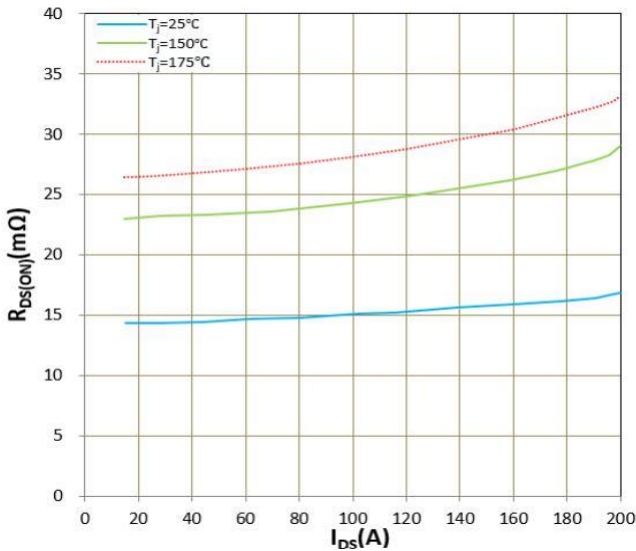


Figure 5. RDS(ON) vs IDS

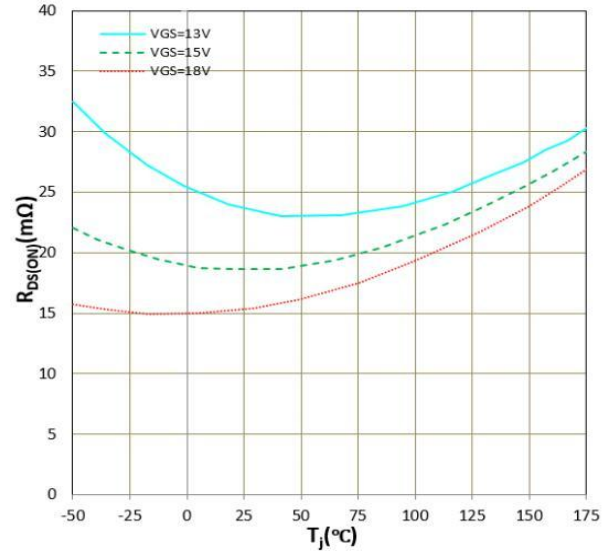


Figure 6. RDS(ON) vs Tj IDS=80A

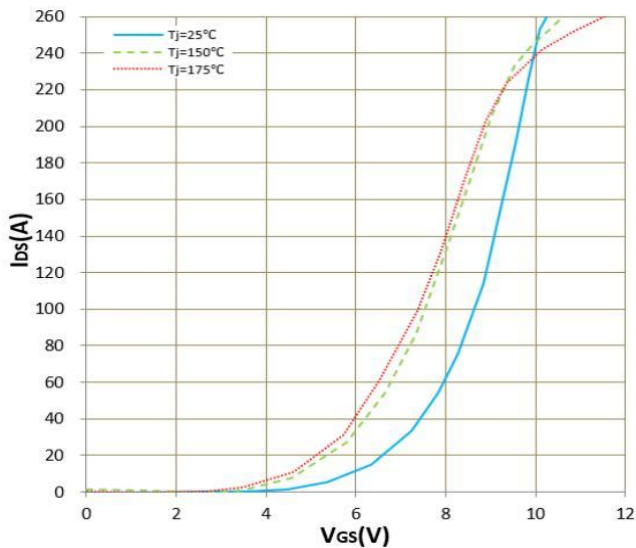


Figure 7. Transfer curves (VDS=20V)

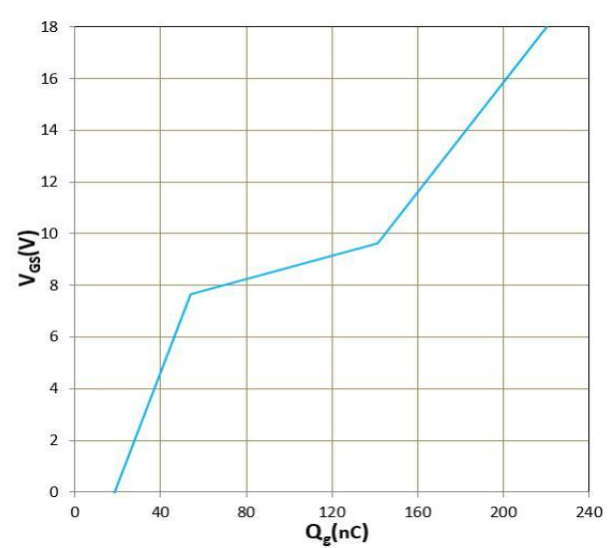
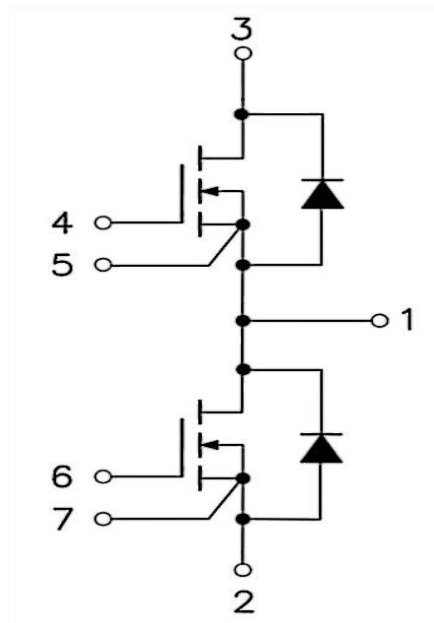
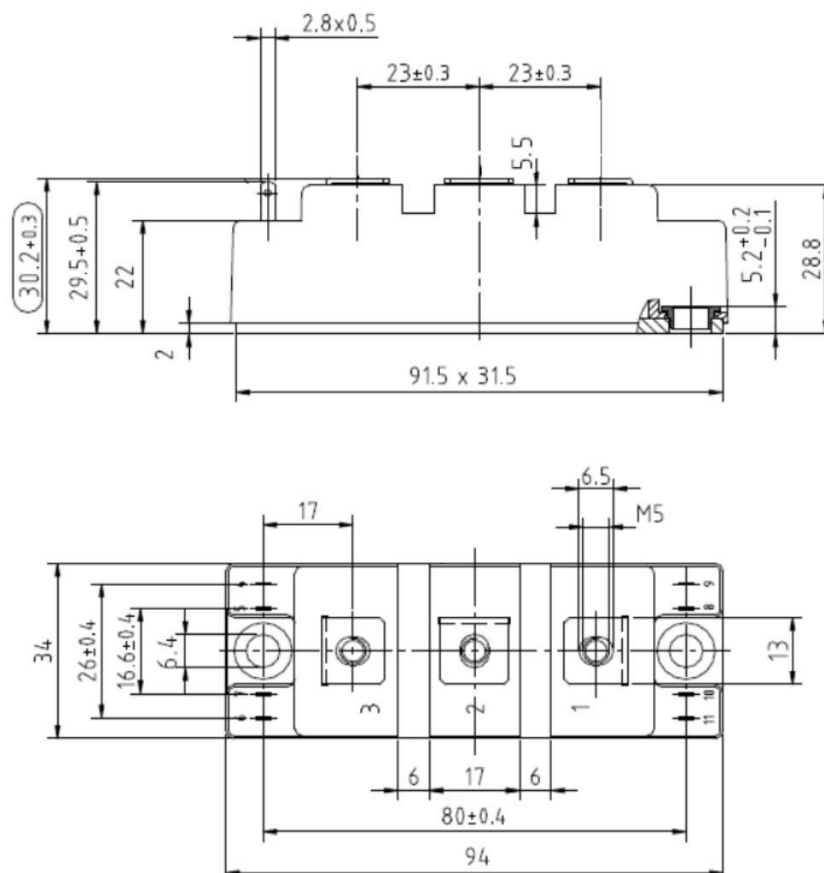


Figure 8. Body diode curves (Tvj=25°C)

Internal Circuit:



Package Outline (Unit: mm):



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