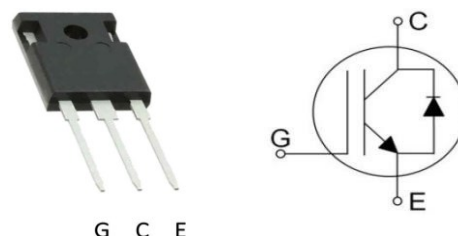


Trench Field-stop IGBT Discretes

Product Summary

Parameter	Value	Unit
VCE	1200	V
IC	40	A
VCE(sat)	1.9	V



Features

- Trench and field-stop technology
- High speed switching
- Low collector to emitter saturation voltage
- Easy parallel switching capability
- High ruggedness performance
- RoHS compliant

Applications

- Welding machines
- UPS
- Solar inverters

Absolute Maximum Ratings

Symbol	Parameter		Value	Unit
VCES	Collector-Emitter Voltage		1200	V
VGES	Gate-Emitter voltage		±20	V
IC	Continuous Collector Current	TC = 25°C	80	A
		TC = 100°C	40	
ICM	Pulsed Collector Current, tp limited by Tvjmax		160	A
IF	Diode Continuous Forward Current TC = 100°C		40	A
IFM	Diode Maximum Current, tp limited by Tvjmax		160	A
Ptot	Power Dissipation (TC=25°C)		681	W
	Power Dissipation (TC=100°C)		340	
Tvj	Operating Junction Temperature		-40 to 175	°C
Tstg	Storage Temperature		-55 to 150	°C

Thermal Resistance

Symbol	Parameter	Typ	Unit
Rth(j-c)	Thermal Resistance, Junction to Case for IGBT	0.22	K /W
Rth(j-c)	Thermal Resistance, Junction to Case for Diode	0.48	
Rth(j-a)	Thermal Resistance, Junction-Ambient	40	

Electrical Characteristics of IGBT (@ TJ = 25°C unless otherwise specified)

Symbol	Parameter	Conditions		Min.	Typ.	Max.	Unit
Static Characteristic							
BVCES	Collector-Emitter Breakdown Voltage	VGE=0V , IC=250uA		1200	-	-	V
ICES	Collector-Emitter Leakage Current	VCE=1200V,VGE=0V		-	-	100	uA
IGES	Gate Leakage Current, Forward	VCE = 0V, VGE = 20V		-	-	100	nA
	Gate Leakage Current, Reverse	VCE = 0V, VGE = -20V		-	-	-100	nA
VGE(th)	Gate-Emitter Threshold Voltage	VGE = VCE, IC = 1mA		5	6	7	V
VCE(sat)	Collector-Emitter Saturation Voltage	VGE=15V,IC=40A	TJ = 25°C	-	1.9	-	V
			TJ = 175°C	-	2.2	-	
Dynamic Characteristic							
Cies	Input Capacitance	VCE=30V, VGE=0V, f=1MHz		-	9900	-	pF
Coes	Output Capacitance			-	189	-	pF
Cres	Reverse Transfer Capacitance			-	40	-	pF
QG	Gate Charge	VCC=960V, IC=40A, VGE=15V		-	263	-	nC
Switching Characteristics							
tD(on)	Turn-On DelayTime	VCC=600V VGE=0/15V IC = 40A, Rg = 10Ω Inductive load		-	85	-	ns
tr	Turn-On Rise Time			-	76	-	ns
tD(off)	Turn-Off DelayTime			-	307	-	ns
tf	Turn-Off Fall Time			-	69	-	ns
Eon	Turn-on Energy			-	2.5	-	mJ
Eoff	Turn-off Energy			-	1.4	-	mJ
Ets	Total switching energy			-	3.9	-	mJ
tD(on)	Turn-On DelayTime	VCC=600V, VGE=0/15V IC = 40A Rg = 10Ω Inductive load Tvj=175°C		-	76	-	ns
tr	Turn-On Rise Time			-	75	-	ns
tD(off)	Turn-Off DelayTime			-	360	-	ns
tf	Turn-Off Fall Time			-	149	-	ns
Eon	Turn-on Energy			-	2.9	-	mJ
Eoff	Turn-off Energy			-	2.6	-	mJ
Ets	Total switching energy			-	5.5	-	mJ
Diode Characteristics							
VF	Diode Forward Voltage	IF= 40A	TJ = 25°C	-	2	-	V
			TJ = 175°C	-	1.6	-	V
Trr	Diode Reverse Recovery Time	VR=400V		-	175	-	ns
Irrm	Diode peak reverse recovery current	IF=40A		-	24	-	A
Qrr	Diode Reverse Recovery Charge	diF/dt=-750A/μs		-	2000	-	nC
Trr	Diode Reverse Recovery Time	VR=400V		-	285	-	ns
Irrm	Diode peak reverse recovery current	IF=40A		-	37	-	A
Qrr	Diode Reverse Recovery Charge	diF/dt=-750A/μs Tvj=175°C		-	5500	-	nC

Characteristics Diagrams

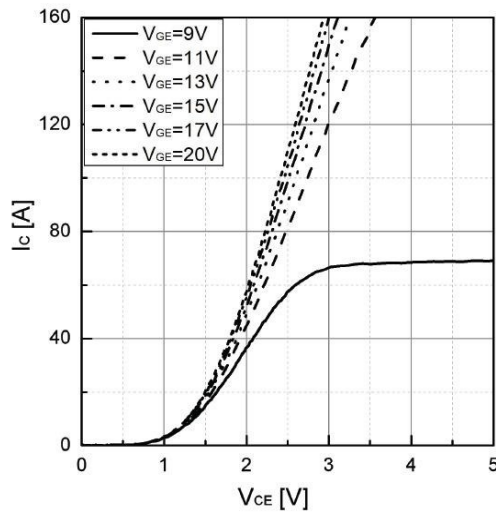


Fig 1. Typical output characteristic ($T_{vj}=25^{\circ}\text{C}$)

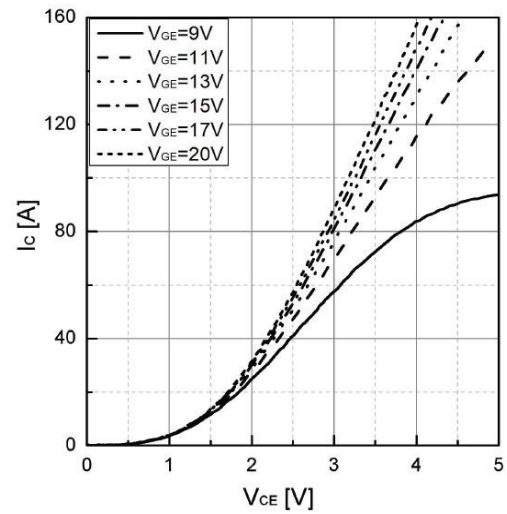


Fig 2. Typical output characteristic ($T_{vj}=175^{\circ}\text{C}$)

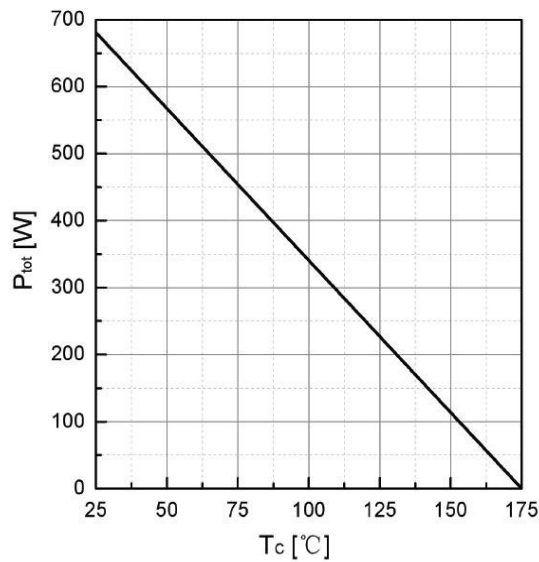


Fig 3. Power dissipation as a function of T_c

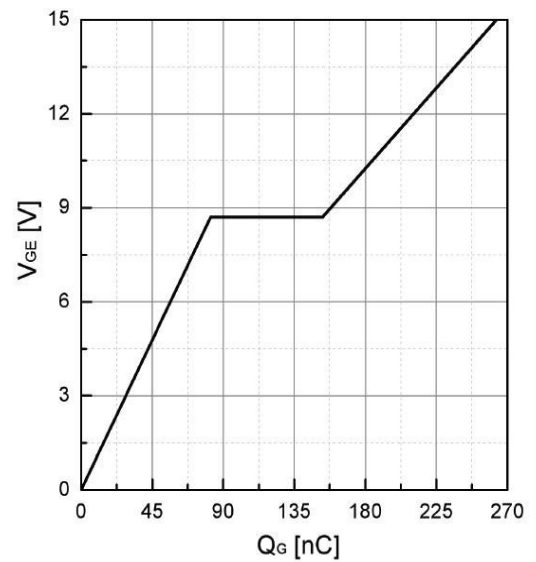


Fig 4. Typical Gate charge

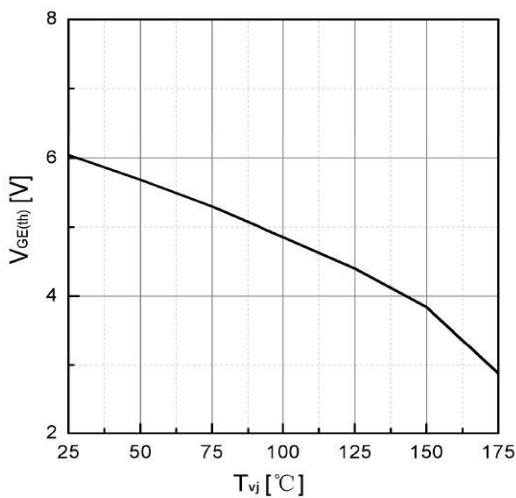


Fig 5. Typical $V_{GE(th)}$ as a function of T_{vj}
($I_C=1\text{mA}$)

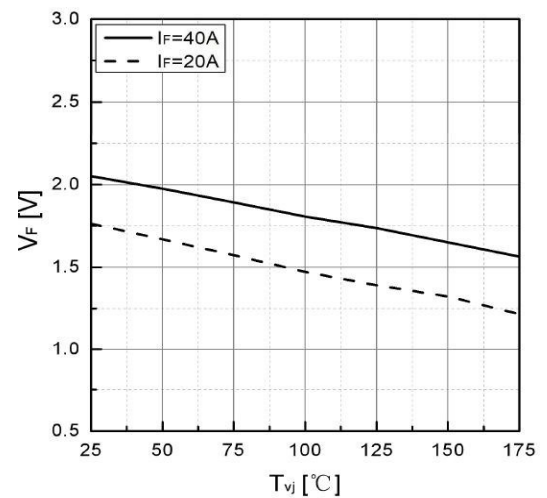


Fig 6. Typical V_F as a function of T_{vj}

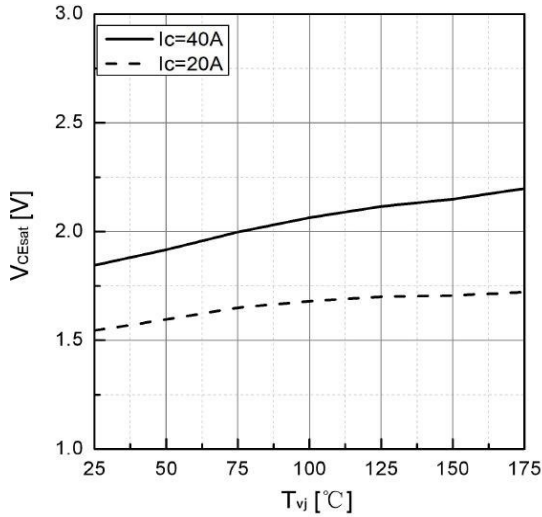


Fig 7. Typical V_{CEsat} as a function of T_{vj}

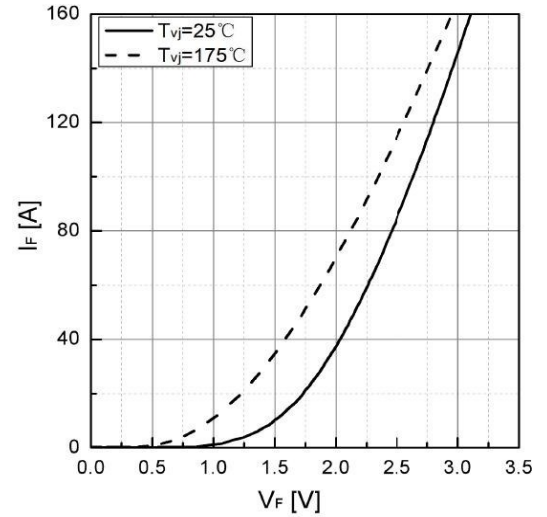


Fig 8. Typical I_F as a function of V_F

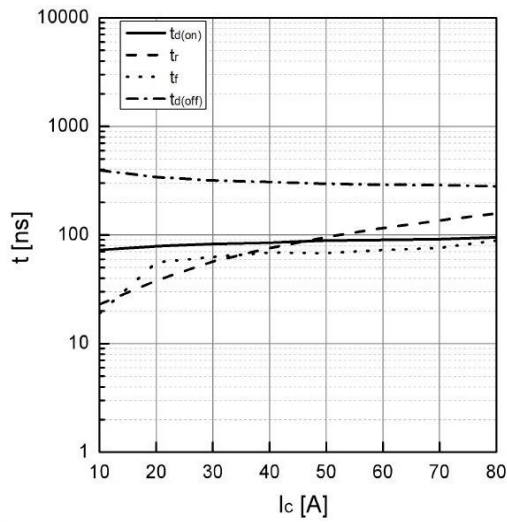


Fig 9. Typical switching time as a function of I_C

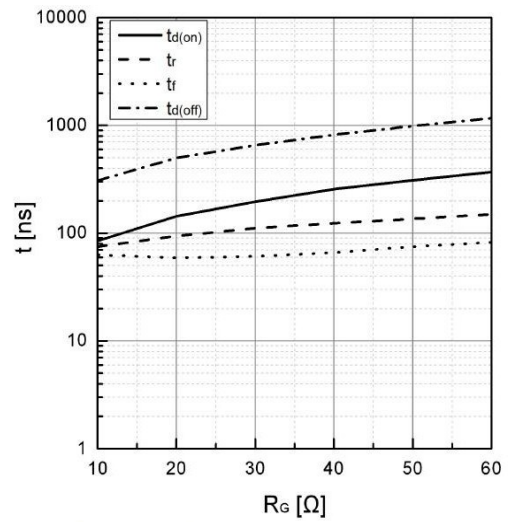


Fig 10. Typical switching times as a function of R_G

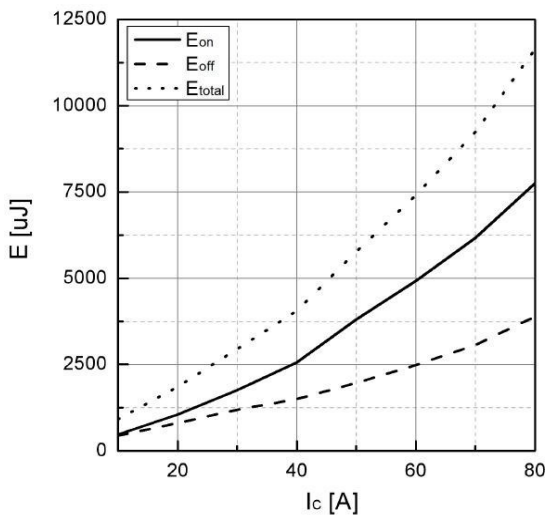


Fig 11. Typical switching energy losses as a function of I_C

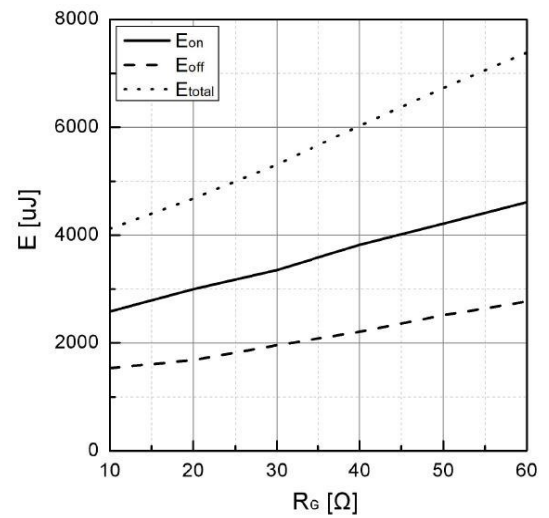


Fig 12. Typical switching energy losses as a function of R_G

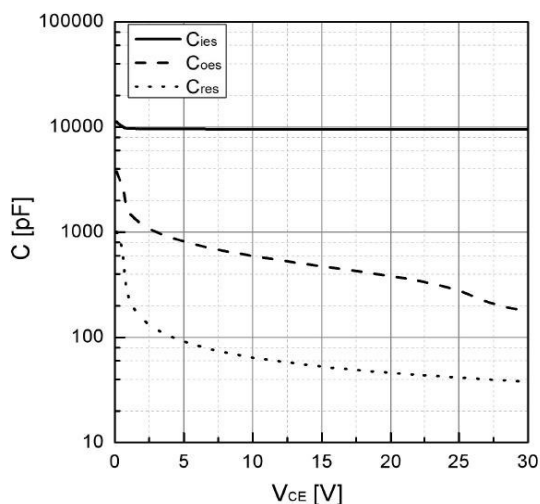


Fig 13. Typical capacitance as a function of V_{CE}
($f=1\text{MHz}$, $V_{GE}=0\text{V}$)

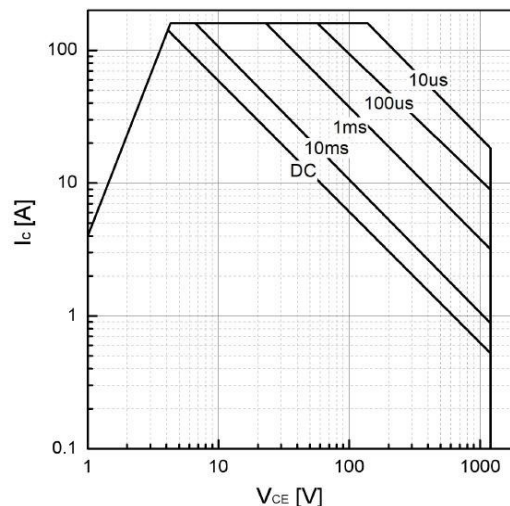


Fig 14. Safe operating area

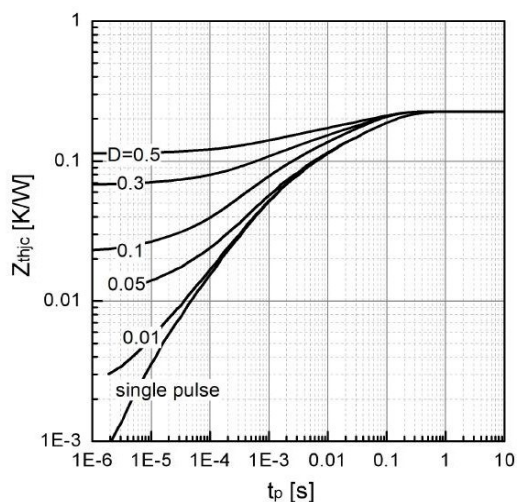
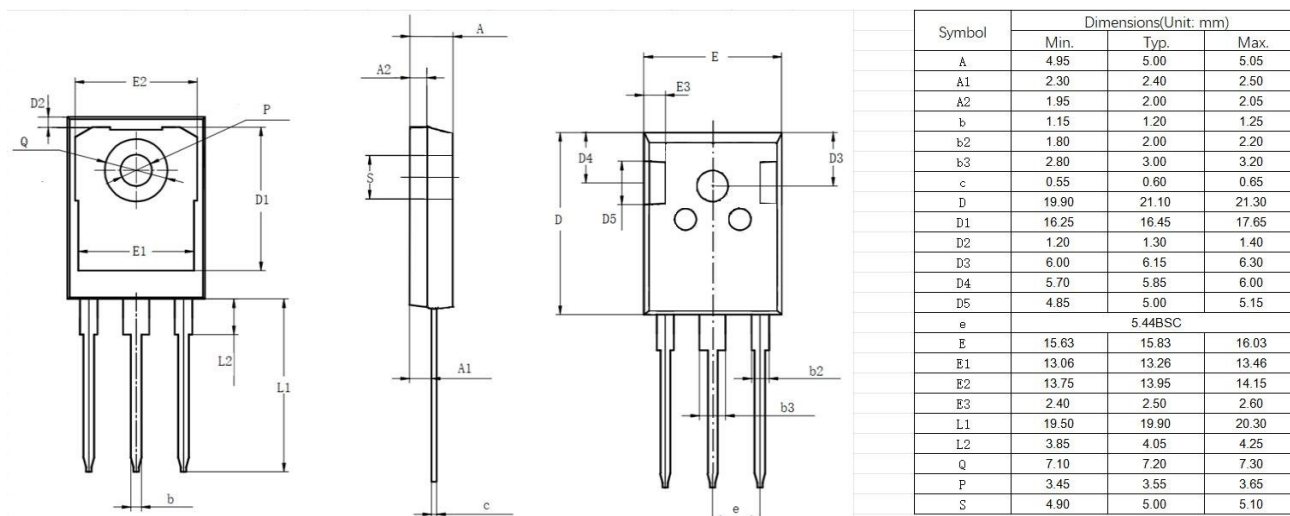


Fig 15. Transient thermal impedance of IGBT

Package Outline



TO-247-3L

NOTICE

Zhuhai Hypersemi Co., Ltd. (hereinafter "Hypersemi") reserves the right to modify, enhance, improve, correct, or otherwise change any product described herein without further notice. Hypersemi shall not be held liable for any consequences arising from the application or use of its products.

While Hypersemi exerts maximum efforts to ensure product quality and reliability, semiconductor devices may malfunction or fail due to inherent electrical sensitivity and susceptibility to physical stress. When utilizing Hypersemi products, the Buyer is obligated to:

- 1) Comply with applicable safety standards in designing a secure system architecture;
- 2) Implement redundancy, fire-prevention measures, and malfunction prevention protocols;
- 3) Mitigate risks of accidents, fires, or societal damages resulting from product use.
- 4) Designers must ensure Hypersemi products operate strictly within specified parameters defined in the latest product specifications.